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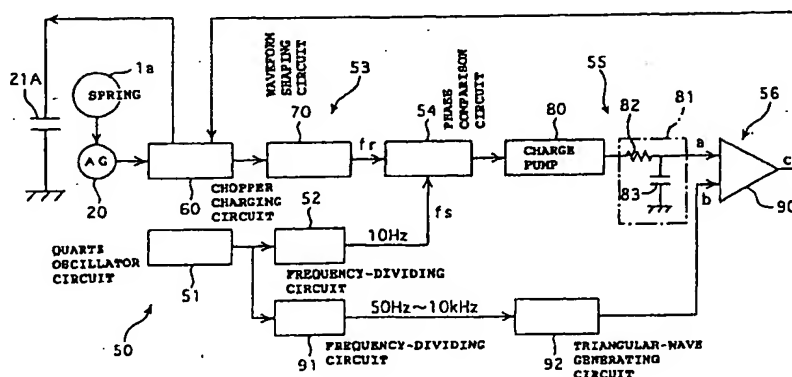
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London WC1N 2ES (GB)(54) **ELECTRONICALLY CONTROLLED MECHANICAL CLOCK AND A METHOD OF CONTROLLING THE SAME**

(57) An electronically controlled mechanical time-piece includes: a voltage-controlled oscillator (25) including a generator that uses a rotor (12) driven by a spring (1a) to rotate, and a brake circuit (23) for controlling the rotation cycle of the generator (20); and a rotation control means (50) for controlling the rotation cycle of the generator (20) by controlling the brake circuit (23). The rotation control means (50) includes a phase-comparison circuit (54) for comparing the phases of rectan-

gular-wave pulses fr output from a VCO (25) and time standard signal fs, and a brake control circuit (56) for inputting, based on an output from the phase-comparison circuit (54), a signal for controlling the brake circuit (23) to a voltage-controlled oscillator (25). By providing the VCO (25) and the phase-comparison circuit (54), PLL control is realized to enable a rapidly responsive system.

FIG. 5



## Description

### Technical Field

[0001] The present invention relates to electronically controlled mechanical timepieces that accurately drive pointers fixed to a train wheel bridge by using a generator to convert mechanical energy in the unwinding mode of a spring into electrical energy, and controlling the rotation cycle of the generator by driving a rotation control means with the electrical energy, and to control methods therefor.

### Background Art

[0002] An electronically controlled mechanical timepiece described in Japanese Examined Patent Publication No. 7-119812 is known as one for indicating an accurate time that accurately drives pointers fixed to a train wheel bridge by using a generator to convert mechanical energy in the unwinding mode of a spring into electrical energy, and controlling a current flowing in the coil of the generator by driving a rotation control means with the electrical energy.

[0003] In the electronically controlled mechanical timepiece, by inputting a signal based on the rotation of the generator into a counter while inputting a signal from a quartz oscillator into the counter, comparing values in the counter, and controlling the generator based on the difference, rotation velocity is controlled. The counter compares the phase differences of reference clock pulses (Ref-pulses) and generator-rotation-cycle pulses (G-pulses), and increases the value of a U/D counter if the G-pulses are ahead, or decreases the value if the G-pulses are behind. The counter consists of a so-called integral counter.

[0004] When a value obtained by measuring the time of one cycle of the Ref-pulses is equal to a value obtained by the integral counter, the braking of the generator is performed, and braking is continuously performed until measurement of the time of one cycle of the Ref-pulses terminates. Accordingly, the value of the integral counter sets a braking release time. That is, the value of the integral counter is multiplied by braking release time N at which the average velocity of the G-pulses is equal to a target velocity (Ref-pulses). In other words, integral control is employed in this system.

[0005] According to the integral control, the average velocity of a rotor over a sufficient duration can be controlled at a velocity in a set time, whereby pointers can be accurately moved at a controlled velocity because signals output in each cycle are compared, while the signals are being counted. However, the integral control has a problem in that the rotation velocity of the rotor cannot be instantly adjusted, which causes low responsiveness. The integral control also has a problem in that a plurality of phase excursions is generated until the relationship between spring force and braking force is

set so as to correspond to a target frequency.

[0006] The integral control can be expressed in the block diagram in Fig. 20.

[0007] In general, it is known that a transfer function used for a generator or motor is  $1/s(sT + 1)$ . This consists of a first-order-lag transfer function 101 and an integral term 102 of  $1/s$ . Accordingly, an integral factor is included in the generator as an object to be controlled. Bode diagrams on the assumption that only the integral control is performed for the object are shown in Figs. 21 and 22.

[0008] In the Bode diagrams, it is required as a condition for stable rotation control that a phase allowance, i.e., the phase at a gain of zero db (gain intersection), be ahead of  $-180^\circ$  and that gain allowance, i.e., the gain at a phase of  $-180^\circ$  (phase intersection), be not more than zero db.

[0009] However, in the case where only the integral control is performed, a phase delay of  $-90^\circ$  occurs in the object, and a further phase delay of  $-90^\circ$  occurs due to the integral control, as shown in Fig. 21, so that the phase is at approximately  $-180^\circ$ . Thus, stable control is difficult because the integral control alone cannot obtain phase allowance and gain allowance. Accordingly, the timepiece in Japanese Examined Patent Publication No. 7-119812 must perform control at a very low frequency, and its responsive characteristic is positioned at approximately 0.016 Hz or less.

[0010] A case where the gain of the integral counter is set to be 100 times greater is shown in Fig. 22. Also, in this case, phase allowance is behind  $-180^\circ$ , and stable control cannot be anticipated.

[0011] As is clear from the above-described data, by performing control using only the conventional integral control, average velocity control can be performed, but a problem occurs in that phase excursions cannot be eliminated.

[0012] In addition, slow control response causes a problem in that almost nothing can be performed for a rapid disturbance as in the case when acceleration is generated in a watch by a swing of an arm.

[0013] It is an object of the present invention to provide an electronically controlled mechanical timepiece that is free from phase excursions and has rapid control system response, and to a control method therefor.

### Disclosure of Invention

[0014] According to the present invention, there is provided an electronically controlled mechanical timepiece including: a mechanical energy source; a generator driven by the mechanical energy source connected to the generator via a train wheel bridge, which generates induced electric power for supplying electric energy; a brake circuit for braking the generator; pointers joined to the train wheel bridge; and a rotation control means for controlling the rotation cycle of the generator by controlling the brake circuit; wherein the rotation control means

includes: a rotation detection means for generating a rotation signal of the generator; a target-signal generating means for generating a target signal corresponding to a target number of revolutions; and a phase-difference compensation means for detecting the phase difference of the target signal output from the rotation detection means, and the target signal output from the target-signal generating means, and outputting a phase-difference compensation signal which is used as a braking control signal in the brake circuit.

[0015] An electronically controlled mechanical timepiece of the present invention uses a mechanical energy source such as a spring to drive pointers and a generator, and controls the number of rotations of a rotor, i.e., the pointers by braking the generator. At this time, the electronically controlled mechanical timepiece compares the phases of a rotation signal of the generator and a target signal such as a timepiece's standard signal, and inputs based on the phase difference a brake control signal to a brake circuit for the generator, whereby a so-called phase-synchronization circuit control (PLL control) is realized in an electronically controlled mechanical timepiece. Accordingly, since a braking level can be set by comparing the waveforms of generated power in each cycle, the activation of a locked range realizes a stable, rapidly responsive system and enables the elimination of phase excursions unless the waveforms of generated power remarkably change in a moment.

[0016] Under these circumstances, it is preferable that the generator and the brake circuit constitute a voltage-controlled oscillator, and that the phase-difference compensation means include: a phase-comparison circuit for comparing the phases of the rotation signal and the target signal; and a brake control means for inputting to the voltage-controlled oscillator the phase-difference compensation signal for controlling the brake circuit based on an output from the phase-comparison circuit.

[0017] Under these circumstances, it is preferable that the rotation control means include a waveform shaping circuit for converting the output waveform of the voltage-controlled oscillator into rectangular-wave pulses, and outputting them as the rotation signal to the phase-comparison circuit.

[0018] The output waveform of the voltage-controlled oscillator changes in accordance with a control method therefor. However, by providing the waveform shaping circuit, the different part of the output waveform can be absorbed, and rectangular-wave pulses capable of being compared with a time standard signal can be output to the phase-comparison circuit, irrespective of the output waveform from the voltage-controlled oscillator. Thus, the phase-comparison circuit, etc., can be used in common to enable a reduction in a component cost.

[0019] In addition, it is preferable that the rotation control means include a frequency-to-velocity converter for converting the frequency of an output signal from the voltage-controlled oscillator into velocity, and that the

brake control means be capable of outputting a signal which controls the brake circuit, based on an output from the phase-comparison circuit and an output from the frequency-to-velocity converter:

[0020] By providing the frequency-to-velocity converter, the time constant of a control circuit can be reduced, and responsiveness can be further improved.

[0021] Moreover, it is preferable that the phase-difference compensation means include phase-difference detection means and compensation-signal generating means for receiving an output from the phase-difference detection means, that the rotation signal and the target signal be repetitive pulses, that the phase-difference detection means include counters for counting the numbers of rises or falls of the respective signals, and that one counter be incremented or decremented when the target signal rises or falls, or is incremented or decremented when the rotation signal rises or falls, and outputs an output of the counter as a phase-difference signal to the compensation-signal generating means.

[0022] By using a counter to constitute the phase-difference detection means, the circuit arrangement can be simplified, and a cost can be also reduced. In addition, a counter capable of holding a plurality of counts can be used. Thus, a phase difference in a broad range can be detected, and even if phase differences are totaled, the total can be held. Accordingly, control in accordance with a total of phase differences can be performed, and more accurate velocity-controlled control can be performed.

[0023] Under these circumstances, it is preferable that the phase-difference detection means include: an integral counter for measuring a total of the phase excursion periods of the rotation signal and the target signal; a proportional counter for measuring phase excursion periods; and an adder for increasing or reducing the value of each counter in accordance with a lead or lag of the phase excursion of the rotation signal with respect to the target signal.

[0024] According to the present invention, there is provided a control method for an electronically controlled mechanical timepiece including a mechanical energy source; a generator driven by the mechanical energy source connected to the generator via a train wheel bridge, which generates induced electric power for supplying electric energy, a brake circuit for braking the generator, pointers joined to the train wheel bridge, and a rotation control means for controlling the rotation cycle of the generator by controlling the brake circuit, wherein a rotation signal of the generator and a target signal generated in accordance with a target number of revolutions are compared to detect the phase difference therebetween, and a phase-difference compensation signal in accordance with the phase difference is used to control the brake circuit.

[0025] In the present invention, phase excursions can be eliminated, and a stable, rapidly responsive control system is realized because an electronically controlled

mechanical timepiece can be controlled by phase-synchronization circuit control (PLL control).

[0026] Under these circumstances, it is preferable that the control method comprise: using an integral counter to measure a total of the phase excursion periods of the rotation signal and the target signal, and using a proportional counter to measure phase excursion periods; determining a lead or lag of the phase excursion of the rotation signal with respect to the target signal; computing a phase-difference compensation signal for setting a braking time by increasing or reducing the value of each counter in accordance with the determined result; and using the phase-difference compensation signal to control the brake circuit. The setting of the braking time includes not only the case where a braking time is directly set, but also the case where indirect braking is performed by setting a braking release time in which braking is not performed in a predetermined cycle.

#### Brief Description of the Drawings

[0027]

Fig. 1 is a plan view showing main components of an electronically controlled mechanical timepiece according to a first embodiment of the present invention.

Fig. 2 is a cross-sectional view showing a main part in Fig. 1.

Fig. 3 is a cross-sectional view showing a main part in Fig. 1.

Fig. 4 is a block diagram showing functions of a first embodiment of the present invention.

Fig. 5 is a block diagram showing the construction of a first embodiment of the present invention.

Fig. 6 is a circuit diagram showing a chopper charging circuit according to a first embodiment of the present invention.

Fig. 7 is a block diagram showing an example of a waveform shaping circuit according to a first embodiment of the present invention.

Fig. 8 is a block diagram showing another example of a waveform shaping circuit according to a first embodiment of the present invention.

Fig. 9 is a waveform chart according to a first embodiment of the present invention.

Fig. 10 is a drawing showing processing by a comparator in a brake control circuit according to a first embodiment of the present invention.

Fig. 11 is a block diagram showing main components of a second embodiment of the present invention.

Fig. 12 is a flowchart showing a control method according to a second embodiment.

Fig. 13 is a flowchart showing the continuation of control in the flowchart in Fig. 12.

Fig. 14 is a flowchart showing the I-value computation flow in Fig. 12.

Fig. 15 is a flowchart showing the P-value computation flow in Fig. 12.

Fig. 16 is a Bode diagram according to a second embodiment.

Fig. 17 is a block diagram showing a modification of the present invention.

Fig. 18 is a circuit diagram showing a modification of a chopper charging circuit according to the present invention.

Fig. 19 is a block diagram showing a modification of a waveform shaping circuit according to the present invention.

Fig. 20 is a block diagram showing the construction of a conventional example related to the present invention.

Fig. 21 is a Bode diagram according to a conventional example.

Fig. 22 is a Bode diagram according to a conventional example.

#### Best Mode for Carrying Out the Invention

[0028] In order that the present invention may be described in more detail, it will be described with reference to the attached drawings.

[0029] Fig. 1 is a plan view showing main parts of an electronically controlled mechanical timepiece according to a first embodiment of the present invention, and Fig. 2 and Fig. 3 are section views of the timepiece.

[0030] The electronically controlled mechanical timepiece includes a barrel drum 1 comprised of a spring 1a, a barrel gear 1b, a barrel arbor 1c, and a barrel cover 1d. The spring 1a has an exterior end fixed to the barrel gear 1b, and an interior end fixed to the barrel arbor 1c. The barrel arbor 1c is supported by a main plate 2 and a train wheel bridge 3, and is fixed by a ratchet fastener 5 so as to rotate together with a ratchet wheel 4.

[0031] The ratchet wheel 4 is engaged with a detent 6 so as to rotate clockwise but not to rotate anticlockwise. A description of a technique for winding the spring 1a by rotating the ratchet wheel 4 clockwise is omitted since the technique is similar to that in the self-winding or hand-winding mechanism of a mechanical timepiece. The rotation of the barrel gear 1b is transmitted to a center wheel 7 at a speed increased 7 times. Successively, it is transmitted to a third wheel 8 at a speed increased 6.4 times. It is transmitted to a second wheel 9 at a speed increased 9.375 times. It is transmitted to a fifth wheel 10 at a speed increased 3 times. It is transmitted to a sixth wheel 11 at a speed increased 10 times. It is transmitted to a rotor 12 at a speed increased 10 times. In all, the speed of the rotation is increased 126000 times.

[0032] A cannon pinion 7a is fixed to the center wheel 7. A minute hand 13 is fixed to the cannon pinion 7a. A second hand 14 is fixed to the second wheel 9. Accordingly, in order that the center wheel 7 may be rotated at 1 rph and that the second wheel 9 may be rotated at 1

rpm, a rotor 12 may be controlled so as to rotate at 5 rps. At this time, the barrel gear 1b rotates at 1/7 rph.

[0033] The electronically controlled mechanical timepiece includes a generator 20 comprised of the rotor 12, a stator 15, and a coil block 16. The rotor 12 consists of a rotor magnet 12a, a rotor pinion 12b, and a rotor inertia disk 12c. The rotor inertia disk 12c is for suppressing a change in the revolution of the rotor 12 against a change in driving torque from the barrel drum 1. The stator 15 is a stator base 15a with forty thousand turns of a stator coil 15b wound around it.

[0034] The coil block 16 is a magnetic core 16a with eleven thousand turns of a coil 16b wound around it. The stator base 15a and the magnetic core 16a are made of PC permalloy or the like. The stator coil 15b and the coil 16b are connected in series so that an output voltage as the sum of voltages generated by them is obtained.

[0035] Next, the control circuit of the electronically controlled mechanical timepiece is described with reference to Figs. 4 to 9.

[0036] In Fig. 4, a block diagram showing the functions of this embodiment is shown.

[0037] An ac output from the generator 20 is increased in voltage and rectified by a rectifying circuit 21 having voltage-raising rectification, full-wave rectification, half-wave rectification, transistor rectification, etc. A load 22 such as an IC for controlling a rotation control means or the like, and a quartz oscillator or the like, is connected to the rectifying circuit 21. In Fig. 4, for convenience of explanation, each functional circuit included in the IC is described separately from the load 22.

[0038] A brake circuit 23 formed by connecting in series a braking resistor 23A and an N-ch or P-ch transistor 23B is connected to the generator 20. The generator 20 and the brake circuit 23 constitute a voltage-controlled oscillator (VCO) 25. In the brake circuit 23, other than the braking resistor 23A, a diode may be inserted if necessary.

[0039] A rotation control means 50 is connected to the VCO 25.

[0040] The rotation control means 50 includes an oscillator circuit 51, a frequency-dividing circuit 52, a rotation detection circuit 53 for the rotor 12, a phase comparison circuit (PC) 54, a low-pass filter (LPF) 55, and a brake control circuit 56.

[0041] The oscillator circuit 51 uses a quartz oscillator 51A to output an oscillating signal, and the oscillating signal is frequency-divided by the frequency-dividing circuit 52 to have a constant cycle. The frequency-divided signal is output as, for example, a 10-Hz time-standard signal (reference cycle signal)  $f_s$  to the phase comparison circuit 54. The time-standard signal (reference cycle signal)  $f_s$  is used as a target revolution of the generator 20. Thus, the oscillator circuit 51 and the frequency-dividing circuit 52 constitute a target-signal generating means for generating a target signal (time

standard signal  $f_s$ ) corresponding to a target revolution.

[0042] The rotation detection circuit 53 receives an output waveform from the VCO 25 at a high impedance so that the generator 20 is not affected. It performs the waveform processing of the output to form rectangular-wave pulses  $f_r$ , and outputs them to the phase comparison circuit 54. Accordingly, using the revolution detection circuit 53 forms a rotation detection means for generating a rotation signal synchronized with the rotation of the generator 20.

[0043] The phase comparison circuit 54 compares the phases of time reference signal  $f_s$  from the frequency-dividing circuit 52 and rectangular-wave pulses  $f_r$  from the rotation detection circuit 53, and outputs a difference signal. The difference signal is processed by the LPF 55 so that its high-frequency components are eliminated, before being input to the brake control circuit 56 as the brake control means.

[0044] The brake control circuit 56 inputs, based on the signal, to the VCO 25, a control signal from the brake circuit 23. Thereby, phase synchronization control is realized.

[0045] Accordingly, the phase comparison circuit 54, the LPF 55, and the brake control circuit 56 constitute a phase-difference compensation means for detecting the phase difference between the rotation signal output from the rotation detection means and the target signal from the target-signal generating means, and outputting a phase-compensation signal that becomes the brake control signal in the brake circuit 23.

[0046] A more detailed construction of this embodiment is shown in Fig. 5 & 6.

[0047] As shown in Fig. 5 & 6, in this embodiment, a chopper charging circuit 60 is used as the brake circuit 23. The chopper charging circuit 60 includes: two comparators 61 and 62 connected to the coils 15b and 16b in the generator 20; a power supply 63 for supplying comparison reference voltage  $V_{ref}$  to the comparators 61 and 62; OR circuits 64 and 65 for outputting the logical sums of outputs from the comparators 61 and 62, and a clock output (control signal) from the brake control circuit 56; field effect transistors (FETs) 66 and 67 which are connected to the coils 15b and 16b and which have gates connected to the outputs of the OR circuits 64 and 65; and diodes 68 and 69 connected to a capacitor 21A provided in the rectifying circuit 21. The FETs 66 and 67 are provided with parasitic diodes 66A and 67A.

[0048] The capacitor 21A has a positive electrode set to have voltage VDD, and a negative electrode set to have VTKN (V/TANK/Negative: Negative electrode of battery). Similarly, the negative electrode of the power supply 63, and the sources of the transistors 66 and 67, are set to have VTKN. Accordingly, in the chopper charging circuit 60, the transistors 66 and 67 are controlled so that the generator 60 is short-circuited to VTKN, whereby chopper boosting is performed so that when the generator 60 is open, it obtains voltage VDD or greater. Therefore, the comparators 61 and 62 com-

pare an increased, generated voltage and arbitrarily set voltage  $V_{ref}$  between VDD and VTKN.

[0049] In the chopper charging circuit 60, outputs from the respective comparators 61 and 62 are supplied to also a waveform shaping circuit 70. Accordingly, the chopper charging circuit 60 and the waveform shaping circuit 70 constitute the rotation detection circuit 53.

[0050] A monostable multivibrator (single shot type) 71 as shown in Fig. 7, a type using a counter 74 and a latch 75 as shown in Fig. 8, etc., are used as the waveform shaping circuit 70.

[0051] The phase comparison circuit 54 includes an analog phase comparator, and a digital phase comparator. By way of example, CMOS-used CMOS-type phase comparators, etc., can be used. The phase comparison circuit 54 detects the phase difference between 10-Hz time-standard signal  $f_s$  from the frequency-dividing circuit 52, and rectangular-wave pulses  $f_r$  from the waveform shaping circuit 70, and outputs a difference signal.

[0052] The difference signal is input to a charge pump (CP) 80, in which it is converted into a voltage level, and the voltage level is processed by a loop filter 81 composed of a resistor 82 and a capacitor 83 so that its high-frequency components are eliminated. Accordingly, the charge pump 80 and the loop filter 81 constitute the LPF 55.

[0053] Level signal  $a$  output from the loop filter 81 is input to a comparator 90. Triangular waves  $b$ , obtained by converting a signal by a frequency-dividing circuit 91 for performing frequency-division to 50 Hz to 100 kHz, and a triangular-wave generating circuit 92 using an integrator, etc., are input to the comparator 90. The comparator 90 outputs rectangular pulse signal  $c$  based on level signal  $a$  from the loop filter 81 and triangular wave signal  $b$ . Therefore, the comparator 90, the frequency-dividing circuit 91, and the triangular-wave generating circuit 92 constitute the brake control circuit 56.

[0054] Rectangular pulse signal  $c$  output from the comparator 90 is input as clock signal CLK to the chopper charging circuit 60 as described above.

[0055] Next, the operation of this embodiment is described with reference to Figs. 9 and 10.

[0056] When the rotor 12 of the generator 20 rotates by the spring 1a, the respective coils 15b and 16b output alternating-current waveforms in accordance with change in magnetic flux. The waveforms are input to the respective comparators 61 and 62. Each comparator 61 or 62 compares the input with voltage  $V_{ref}$  from the power supply 63. The comparisons by the comparators 61 and 62 detect timing of a polarity for switching on the transistors 66 and 67.

[0057] In other words, the voltage-increasing charging of the capacitor 21A, and the chopper-braking of the generator 20 can be performed by only inputting clock signal CLK to the gates of the transistors 66 and 67. In the case where control is performed using only clock signal, when the clock signal is at its Hi-level, the transistors 66 and 67 are simultaneously activated to gener-

ate short-circuiting, and when the clock signal is at its Lo-level, it passes through one of the parasitic diodes 66A and 67A and one of the diodes 68 and 69 to charge the capacitor 21A. Specifically, when AG1 is positive, charging is performed using a path from the parasitic diode 67A to the diode 68 via the coils 15b and 16b, and when AG2 is positive, charging is performed using a path from the parasitic diode 66A to the diode 69 via the coils 15b and 16b.

[0058] In this case, two diodes are connected in series in each charging path, a voltage drop for the sum of the rise voltages  $V_F$  of the respective diodes is generated. Therefore, the capacitor 21A cannot be charged unless the charging voltage is not less than a voltage obtained by adding the voltage drop to the potential of the capacitor 21A. This is a significant factor decreasing charging efficiency in the case of a generator whose generated voltage is small, such as an electronically controlled mechanical timepiece.

[0059] Accordingly, in this embodiment, the transistors 66 and 67 are not switched on or off, but timing therefor is controlled, whereby charging efficiency is increased.

[0060] In other words, when AG1 is positive with respect to VTKN, and is more than voltage  $V_{ref}$ , the comparator 62 outputs a Hi-level signal. Thus, the OR circuit 65 continuously outputs the Hi-level signal, irrespective of clock signal CLK, whereby a voltage is applied to the gate of the transistor 67, and the transistor 67 is switched on.

[0061] In addition, the comparator 61 connected on the AG1 side outputs a Lo-level signal because AG2 < voltage  $V_{ref}$ . The OR circuit 64 outputs a signal synchronized with the clock signal. The transistor 66 continuously performs on/off operation. The chopper voltage increasing in terminal AG1 is performed.

[0062] At this time, when the transistor 66 is switched off after being switched on, a charging path of AG1 to the diode 68 to the capacitor 21A to VTKN to the transistor 67 (its source to drain) to AG2 is formed, and the parasitic transistor 67A is positioned off the path. Thus, a voltage drop is small, and charging efficiency is improved.

[0063] Concerning the level of voltage  $V_{ref}$ , it is preferable to select a generated voltage level that performs the chopper voltage increasing of a voltage generated by the generator 20 so that the capacitor 21A can be charged. Normally, the voltage level may be set at a level that is several hundred mV greater than VTKN. When voltage  $V_{ref}$  is set at a high level, a period until the comparators 61 and 62 operate is long, and during the period a charging path in which two diodes are connected in series as described above is formed, so that power generating efficiency decreases by that amount.

[0064] When the transistor 66 is switched on, short-circuiting in the generator 20 occurs to cause short braking since the transistor 67 is also switched on, and the quantity of generated power decreases. However, by short-circuiting the generator 20 on the VTKN side,



voltage increasing to a voltage not less VDD can be performed when the transistor 66 is open. Thus, by setting the cycle of chopping for switching on/off, at a predetermined cycle, a decrease in the quantity of power generated when short braking is performed can be compensated, and braking torque can be increased with the generated power maintained at a constant or greater.

[0065] Also, in the case where the output from the generator 20 is directed to the AG2 side, the operations of the comparators 61 and 62, and the transistors 66 and 67, are only switched, and operations similar to those described above are performed. The outputs of the comparators 61 and 62 in the chopper charging circuit 60 are input to the waveform shaping circuit 70, and are converted into rectangular-wave pulses fr.

[0066] By way of example, the monostable multivibrator 71 in Fig. 7 performs waveform shaping based on only one polarity detection result (the output of the comparator 62). Specifically, only at the rise of the output of the comparator 62, the monostable multivibrator 71 is triggered, and pulses having a length set by CR are output. The CR time constant is set at not less than approximately 1.5 times as long as one cycle of clock signal CLK. Within the pulse time set by CR, the rise of the next output of the comparator 62 is input to the monostable multivibrator 71, and it is retriggered. Accordingly, the monostable multivibrator 71 continuously outputs a hi-level signal until the rise of the output of the comparator 62 is not generated within time 1.5T set by CR, whereby rectangular-wave pulses fr corresponding to an output signal of the generator 20 are output. The rise time of pulses fr is delayed by the CR setting time - the Hi-level period of the polarity detection pulses, and as shown in Fig. 9, when CR is 1.5T, a delay is generated by  $1.5T - 0.5T = 1T$ .

[0067] Also the waveform shaping circuit 70 shown in Fig. 8 performs waveform shaping based on only one polarity detection result (the output of either the comparator 61 or 62). Specifically, the waveform shaping circuit 70 includes a counter 74 for counting the clock signals for time 2T and clearing the count, and a latch means 75 for performing latching with an output from the counter 74. The counter 74 and the latch means 75 are reset by the output of either the comparator 61 or 62. By way of example, in the case where an output from the comparator 62 is generated as shown in Fig. 9, the latch means 75 and the counter 74 are reset, and output fr is output as a Lo-level signal. When an output from the comparator 62 is not generated, output fr is latched at its Hi-level.

[0068] When the output from the comparator 62 is generated again, the latch signal is cleared, and output fr is at its Lo-level, so that rectangular wave pulses can be obtained. When the output from the comparator 62 is generated within the counter set time (2T), latching is not performed. Also, in this case, as shown in Fig. 9, the rise of the Hi-level of rectangular wave pulses fr is

delayed by the set time (2T) of the counter 74.

[0069] The waveform shaping circuits 70 in Figs. 7 and 8 perform conversion into rectangular wave pulses by delaying the output of the comparator 62. This prevents pulse deformation by using the CR set time and the counter set time because, since the output from the comparator 62 cannot always be obtained as a signal synchronized with the cycle of the clock signal, but as a so-called pulse-missing output, direct conversion into rectangular wave pulses causes pulse deformation. The CR set time and the counter set time may be determined depending on the amount of pulse deformation, and may be set at approximately cycle 1.5 to 5T. In a control aspect, such a delay almost does not cause adverse effects.

[0070] Rectangular wave pulses fr shaped as described above are compared with time standard signal fs of the frequency-dividing circuit 52 by the phase comparison circuit 54, and the difference signal is converted into level signal a through the charge pump 80 and the loop filter 81.

[0071] The comparator 90 outputs rectangular-wave-pulse signal c, using level signal a and triangular signal b from the triangular-wave generating circuit 92, as shown in Fig. 10. Level signal a is set so that it is lower than a standard level when rectangular-wave pulses fr based on the rotation of the rotor 12 is ahead of time standard signal fs and so that it is higher than the standard level when rectangular-wave pulses fr are behind time standard signal fs.

[0072] Accordingly, in the case where rectangular-wave pulses fr are ahead of time standard signal fs, the H-level state of rectangular-wave-pulse signal c lasts long, and the short brake time of each chopper cycle in the chopper charging circuit 60 increases by the prolonged state, so that braking is enhanced to reduce the velocity of the rotor 12 of the generator 20. Conversely, in the case where rectangular-wave pulses fr are behind time standard signal fs, the L-level state of rectangular-wave-pulse signal c lasts long, and the short brake time of each chopper cycle in the chopper charging circuit 60 decreases by the prolonged state, so that braking is weakened to increase the velocity of the rotor 12 of the generator 20. Thereby, control for rectangular-wave pulses fr to match time standard signal fs is performed.

[0073] According to the above-described embodiment, the following effects are obtained.

- (1) By providing the VCO 25 comprised of the generator 20 and the brake circuit 23, and the phase-difference compensation means comprised of the phase comparison circuit 54 and the brake control circuit 56, PLL control is used to control the rotation of the generator 20. Therefore, since the brake level in the brake circuit 23 can be set by comparing the generated-power waveform for each cycle with another, the activation of a locked range enables stable, rapidly responsive control and the elimina-

tion of phase difference unless the waveform of generated power remarkably changes in a moment.

(2) The brake circuit 23 is formed by using the chopper charging circuit 60, and the use of chopping enables brake control, whereby braking torque can be increased with generated power maintained at a constant or greater. Thereby, while the system stability is being maintained, efficient brake control can be performed.

(3) By using the chopper charging circuit 60, not only brake control but also the charging of the capacitor 21A in the rectifying circuit 21, and detection of the rotation of the rotor 12 of the generator 20, is realized by the chopper charging circuit 60. Compared with the case where the respective functions are realized by using separate circuits, the circuit arrangement can be simplified to reduce the number of components and to reduce a cost, and production efficiency can be improved.

(4) In the chopper charging circuit 60, timing for control of switching each transistor 66 or 67 is adjusted, and with either transistor 66 or 67 switched on, the other is switched on or off, whereby a voltage drop in the charging path can be reduced to improve power generating efficiency. This is therefore effective because in the case where the small generator 20 must be used as particularly in an electronically controlled mechanical timepiece, the power generating efficiency can be improved.

(5) Since the waveform shaping circuit 70 is provided, even if the circuit arrangement of the chopper charging circuit 60, etc., is modified and the output waveform from the VCO 25 differs, the different part of the output waveform can be absorbed by the waveform shaping circuit 70. Accordingly, if the circuit arrangement of the chopper charging circuit 60 differs, the rotation control means 50 can be used in common, and the component cost can be reduced.

(6) In the case where a general circuit composed of a low-pass filter (LPF) and a comparator is used as the waveform shaping circuit 70, an LPF comprised of, e.g., a first-order-lag CR filter, etc., is charged with part of the generated voltage obtained by chopper voltage increasing, which is a factor increasing the efficiency of charging the capacitor 21A. However, each waveform shaping circuit 70 in this embodiment performs digital processing, whereby current consumption can be suppressed to be low, and the efficiency of charging the capacitor 21A can be improved.

[0074] Next, a second embodiment of the present invention is described. In the embodiment, by denoting components similar or identical to the above-described embodiment by identical reference numerals, descriptions are omitted or simplified.

[0075] Concerning an electronically controlled mechanical timepiece according to the embodiment, the specific structure of a rotation control means differs from that in the first embodiment. Specifically, the rotation control means according to the embodiment includes an oscillator circuit (quartz oscillator) 51, a frequency divider 52, a phase-difference compensation means 200, a generator 20, and a rotation detection means 53, as shown in Fig. 11. Other components necessary for an electronically controlled mechanical timepiece are similar to those in the first embodiment.

[0076] The frequency divider 52 outputs first reference clock pulses (Ref1 pulses) and second reference clock pulses (Ref2 pulses). The frequency of the Ref2 pulses is set to be extremely higher than the frequency of the Ref1 pulses.

[0077] The phase-difference compensation means 200 includes a phase-difference detection means 210, and a brake-signal generating means (brake control means) 220 as a phase-difference compensation means. The phase-difference detection means 210 includes an integral counter 211, a proportional counter 212, and an adder 213.

[0078] A method for controlling the generator 20 in the embodiment is described with reference to Figs. 12 to 15.

[0079] Initially, a generator-rotation-cycle pulses (G pulses) from the rotation detection means 53, and the first reference clock pulses (Ref1 pulses) from the frequency divider 52, are compared in phase difference (step 1)(a step is hereinafter abbreviated to "S").

[0080] Next, the integral counter 211 is used to find an I-value (S2), and the proportional counter 212 is used to find a P-value (S3).

[0081] A technique in which the integral counter 211 finds the I-value is performed in accordance with the I-value computation flow shown in Fig. 14. In other words, by comparing the Ref1 pulses and G-pulses as to phase difference within one cycle, it is determined whether the G-pulses are ahead (S21).

[0082] If the G-pulses are ahead in the step, by counting its leading time using integral gain pulses Ci, the leading quantity is measured (S22), and the integral counter (U/D counter) 211 is incremented (S23). If the G-pulses are behind, by counting its lag time using integral gain pulses Ci, the lag is measured (S24), and the integral counter (U/D counter) 211 is decremented (S25). Therefore, a value counted by the integral counter 211 is a multiple of phase excursion time measured using integral gain pulses Ci, and this is used as the I-value (S26).

[0083] In addition, a technique in which the proportional counter 212 finds the P-value is performed in accordance with the P-value computation flow shown in Fig. 15. In other words, phase excursions based on the leading or lag G-pulses are counted using proportional gain pulses Cp, and phase excursion time is measured by the proportional counter (S31). By detecting, for



example, the disappearance of an input from a measurement panel (proportional gain pulses  $C_p$ ), which is input to the counter while the phase difference is being generated, it is determined whether phase excursion measurement ends (S32). When the measurement ends, the proportional counter value is set to the P-value (S33). At this time, specifically, the proportional counter value is stored as the P-value in a register, and the proportional counter is subsequently reset.

[0084] It is determined whether the stored value is a leading excursion (leading phase) (S4). If the stored value is a leading excursion, the operation  $N = I + P$  is executed (S5). If the stored value is a lag excursion, the operation  $N = I - P$  is executed to find the N-value. The N-value sets a braking time in one cycle period of the generator 20, as described below. In the case where the rotation cycle pulses of the generator 20 are ahead of the reference clock pulses, the N-value increases as a result of  $I + P$ , which further increases the braking time. In the case where the rotation cycle pulses of the generator 20 are behind the reference clock pulses, the N-value decrease as a result of  $I - P$ , which further reduces the braking time. Accordingly, the N-value is also used as a phase-difference compensation signal.

[0085] Concerning integral gain pulses  $C_i$  and proportional gain pulses  $C_p$ , the relationship between their frequencies is  $C_i \ll C_p$ . Therefore, the P-value functions so as to actively suppress a phase excursion due to a rapid disturbance, etc., as in the case where when the electronically controlled mechanical timepiece is used as a watch, the motion of an arm causes the rotor 12 or the like to have an acceleration, while the I-value functions so as to control a slow change due to the unwinding of the spring 1a.

[0086] The brake-signal generating means 220 includes a Ref2 counter for measuring the second reference clock pulses (Ref2 pulses). The brake-signal generating means 220 uses the Ref2 counter to start counting the Ref2 pulses while the phase-difference detection means 210 is computing the N-value (S7), and simultaneously performs braking (S8).

[0087] It is determined whether the value by the Ref2 counter is equal to the computed N-value (S9). When these values are equal, the braking of the generator 20 is released (S10).

[0088] Next, by, for example, detecting whether the Ref2 counter is incremented, it is determined whether a predetermined one cycle set for braking control terminates (S11). If the predetermined cycle terminates, the Ref2 counter is stopped to be reset (S12), and the counting of the Ref2 pulses by the Ref2 counter is restarted (S7).

[0089] Specifically, the Ref2 counter consists of, e.g., a 4-bit ripple counter that repeatedly performs re-counting from zero when finishing counting from zero to fifteen. When the Ref2 counter has zero (S7), the braking state is activated (S8), and the braking is continuously performed until the computed N-value is equal to the

Ref2 counter value (S9). When the N-value is equal to the Ref2 counter (S9), the braking is released (S10), and the braking-released state is maintained until the Ref2 counter value has a 4-bit full Count, namely, fifteen (S11). When the Ref2 counter value returns from fifteen to zero (S12), the braking state is activated (S7 and S8).

[0090] Accordingly, the N-value sets the brake time in one cycle period of the generator 20. Thereby, the generator 20 is synchronized with the first reference clock pulses in frequency and phase, with its frequency and phase being close to those of the first reference clock pulses. Thus, the pointers indicate an accurate time.

[0091] In the second embodiment, the second reference clock pulses (Ref2 pulses), in which each frequency has the relationship Ref1 pulses  $\gg$  Ref2 pulses, are used. By using the Ref2 pulses whose frequency is extremely higher than that of the Ref1 pulses, the chopper charging circuit in the first embodiment can be controlled by the Ref2 pulses.

[0092] A Bode diagram in which integral control and proportional control are combined similarly to the first embodiment is shown in Fig. 16. According to the diagram, phase allowance is approximately  $40^\circ$ , gain allowance is not less than -40 db, and the responsive frequency is 0.16 Hz, so that the responsive velocity can be set to be approximately ten times the related art shown in Figs. 21 and 22.

[0093] The second embodiment also uses PI control, similarly to the first embodiment, and accordingly provides operations and effects similar to the operations and effects (1) to (6).

[0094] In addition, (7) the use of the second reference clock pulses (Ref2-pulses) having a high frequency enables the second embodiment to be applied to a control flow for the chopper charging circuit.

[0095] The present invention is not limited to each embodiment, but includes modifications and improvements in a range where the object of the present invention can be achieved.

[0096] By way of example, as shown in Fig. 17, the rotation control means 50 may be provided with a frequency-to-velocity (F/V) converter 100 for converting the output frequency of a waveform shaping circuit 70 into velocity information. By providing the F/v converter 70, rotation velocity information on a generator 20 can be obtained, and the time constant of a control circuit can be reduced, so that improve responsiveness can be reduced, and the rotation velocity of the generator 20 can be controlled so as to quickly approach a set velocity, i.e., a time standard signal. Accordingly, if the waveform of generated power remarkably changes in a moment, and is off a locked range, rapid response can be performed to maintain control, whereby a more stable system can be provided.

[0097] A chopper charging circuit 60 is not limited to the one in the embodiment, but a chopper charging circuit 110 that includes one comparator 111 for detecting the polarity of a rotor 12, a diode 112 for performing the

chopping of transistors 66 and 67, and a resistor 113, as shown in Fig. 18.

[0098] In the embodiment, since the comparators 61 and 62 are used for polarity detection, the power supply 63 for comparing reference voltage  $V_{ref}$  is necessary. However, in this embodiment, the need for the power supply can be eliminated. In the chopper charging circuit 110, in order that the transistors 66 and 67 may be controlled to be in conduction for the polarity of a power-generating coil, the transistors 66 and 67 are driven by passing a coil-end voltage through diodes 112. Accordingly, the coil-end voltage must be increased to be greater than voltage (threshold voltage)  $V_{th}$  capable of driving the transistors 66 and 67 + rise voltage  $V_f$  of the diodes 112. For example, if  $V_{th} = 0.5$  V and diode  $V_f = 0.3$  V, this condition alone requires 0.8 V, and the generator 20 requires a power-generating ability of approximately 1.0 to 1.6 V. For this reason, the chopper charging circuit 60 in the embodiment that drives the transistors 66 and 67 without using the diodes is preferable in that an efficient chopper charging operation from a small generated voltage of the generator 20 can be performed.

[0099] In addition, the chopper-charging-circuit structure may be formed by changing the type of the transistors 66 and 67 of the chopper charging circuit 60 in Fig. 6 to the P-ch type, exchanging them and the diodes 68 and 69, and short-circuiting them to positive VDD of the capacitor 21A for voltage increasing so that when the transistors 66 and 67 are open, their voltage is not more than the voltage of VTKN. In this case, outputs from the comparators 61 and 62, and clock signal CLK, are logically added by an AND circuit, and are input to the gates of the transistors 66 and 67.

[0100] Moreover, the structures of the rotation detection circuit 53, the LPF 55, and the brake control circuit 56 are not limited to ones comprised of the waveform shaping circuit 70, the charge pump 80 and the loop filter 81, the comparator 90, and the frequency-dividing circuit 91 and the triangular-wave generating circuit 92 as in the embodiment, but may be appropriately provided for practicing the present invention.

[0101] By way of example, concerning the waveform shaping circuits 70, a latch means 120 as shown in Fig. 19 may be used. The waveform shaping circuits 70 uses an output from either comparator 61 or 62 to perform the shaping of rectangular-wave pulses  $fr$ , as also shown in Fig. 9. However, a waveform shaping circuit 70 according to Fig. 13 performs the latching of the latch means 120 at the rise of an output from the AG1 polarity detection (comparator 62), and resets the latch means 120 at an output from the comparator 61 for AG2. This case has advantages in that no time delay occurs and accurate detection can be performed, although two outputs need to be used. By performing latching at an output for AG1, if a pulse missing is generated in the output for AG1, it is ignored, which can prevent it from affecting rectangular-wave pulses  $fr$ .

[0102] In addition, the brake circuit 23 is not limited to one using chopping, but may be one that performs braking by using the VCO to change the current flowing in the coils 15b and 16b. It may be appropriately selected for practicing the present invention.

[0103] As described above, according to the present invention, PLL control is realized in an electronically controlled PLL control is realized in an electronically controlled mechanical timepiece, whereby phase excursions can be eliminated, and control-system response can be quickened.

[0104] The present invention is not limited to watches, but may be applied to spring-driven clocks and wall clocks, etc.

#### Industrial Applicability

[0105] As described above, an electronically controlled mechanical timepiece and a control method therefor according to the present invention are useful for various types of electronically controlled mechanical timepieces that use a generator to convert mechanical energy in the unwinding mode of a spring into electrical energy, and drive a rotation control means with the electrical energy, whereby controlling the rotation cycle of the generator, and in particular, to small electronically controlled mechanical timepieces used as watches.

#### Claims

1. An electronically controlled mechanical timepiece including: a mechanical energy source; a generator driven by said mechanical energy source connected to said generator via a train wheel bridge, which generates induced electric power for supplying electric energy; a brake circuit for braking said generator; pointers joined to said train wheel bridge; and rotation control means for controlling the rotation cycle of said generator by controlling said brake circuit; wherein said rotation control means includes: rotation detection means for generating a rotation signal of said generator; target-signal generating means for generating a target signal corresponding to a target number of revolutions; and phase-difference compensation means for detecting the phase difference of said target signal output from said rotation detection means, and said target signal output from said target-signal generating means, and outputting a phase-difference compensation signal which is used as a braking control signal in said brake circuit.
2. An electronically controlled mechanical timepiece according to Claim 1, wherein said generator and said brake circuit constitute a voltage-controlled oscillator, and said phase-difference compensation means includes: a phase-comparison circuit for

comparing the phases of said rotation signal and said target signal; and brake control means for inputting to said voltage-controlled oscillator said phase-difference compensation signal for controlling said brake circuit based on an output from said phase-comparison circuit.

3. An electronically controlled mechanical timepiece according to Claim 2, wherein said rotation control means includes a waveform shaping circuit for converting the output waveform of said voltage-controlled oscillator into rectangular-wave pulses, and outputting them as said rotation signal to said phase-comparison circuit.

4. An electronically controlled mechanical timepiece according to Claim 2 or 3, wherein said rotation control means includes a frequency-to-velocity converter for converting the frequency of an output signal from said voltage-controlled oscillator into velocity, and said brake control means is capable of outputting a signal which controls said brake circuit, based on an output from said phase-comparison circuit and an output from said frequency-to-velocity converter.

5. An electronically controlled mechanical timepiece according to Claim 1, wherein said phase-difference compensation means includes phase-difference detection means and compensation-signal generating means for receiving an output from said phase-difference detection means,

said rotation signal and said target signal are repetitive pulses,  
said phase-difference detection means includes counters for counting the numbers of rises or falls of the respective signals, and one counter is incremented or decremented when said target signal rises or falls, or is incremented or decremented when said rotation signal rises or falls, and outputs an output of the counter as a phase-difference signal to said compensation-signal generating means.

6. An electronically controlled mechanical timepiece according to Claim 5, wherein said phase-difference detection means includes: an integral counter for measuring a total of the phase excursion periods of said rotation signal and said target signal; a proportional counter for measuring phase excursion periods; and an adder for increasing or reducing the value of each counter in accordance with a lead or lag of the phase excursion of said rotation signal with respect to said target signal.

7. A control method for an electronically controlled mechanical timepiece including a mechanical

energy source; a generator driven by said mechanical energy source connected to said generator via a train wheel bridge, which generates induced electric power for supplying electric energy, a brake circuit for braking said generator, pointers joined to said train wheel bridge, and rotation control means for controlling the rotation cycle of said generator by controlling said brake circuit, wherein a rotation signal of said generator and a target signal generated in accordance with a target number of revolutions are compared to detect the phase difference therebetween, and a phase-difference compensation signal in accordance with the phase difference is used to control said brake circuit.

8. A control method for an electronically controlled mechanical timepiece, according to Claim 7, wherein said control method comprises: using an integral counter to measure a total of the phase excursion periods of said rotation signal and said target signal, and using a proportional counter to measure phase excursion periods; determining a lead or lag of the phase excursion of said rotation signal with respect to said target signal; computing a phase-difference compensation signal for setting a braking time by increasing or reducing the value of each counter in accordance with the determined result; and using said phase-difference compensation signal to control said brake circuit.

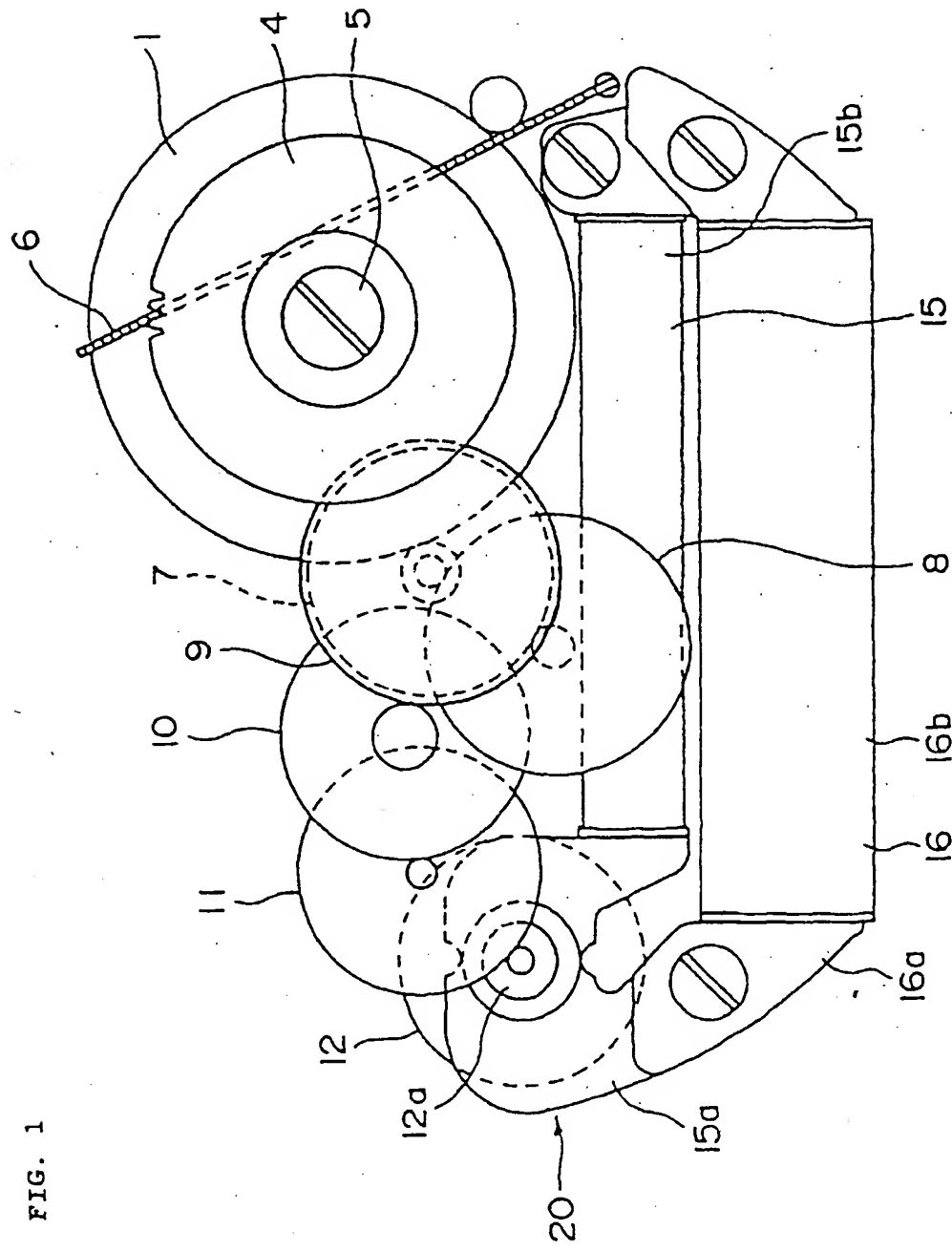


FIG. 1

FIG. 2

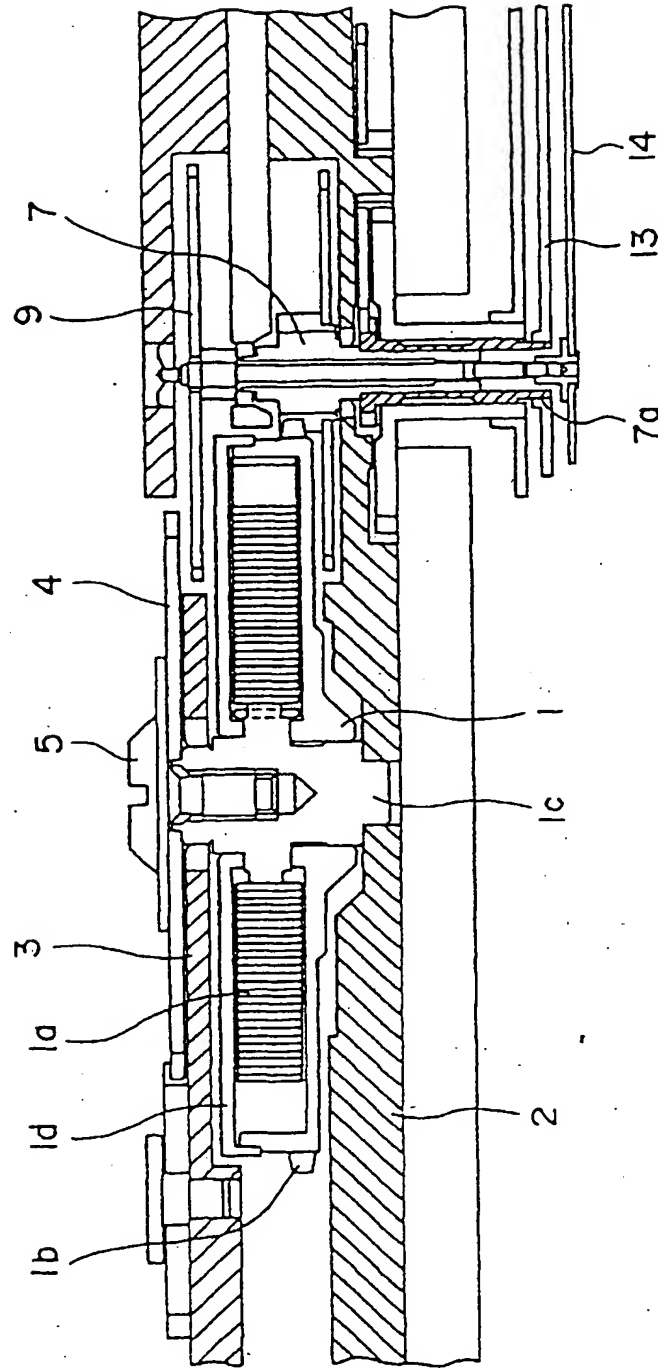


FIG. 3

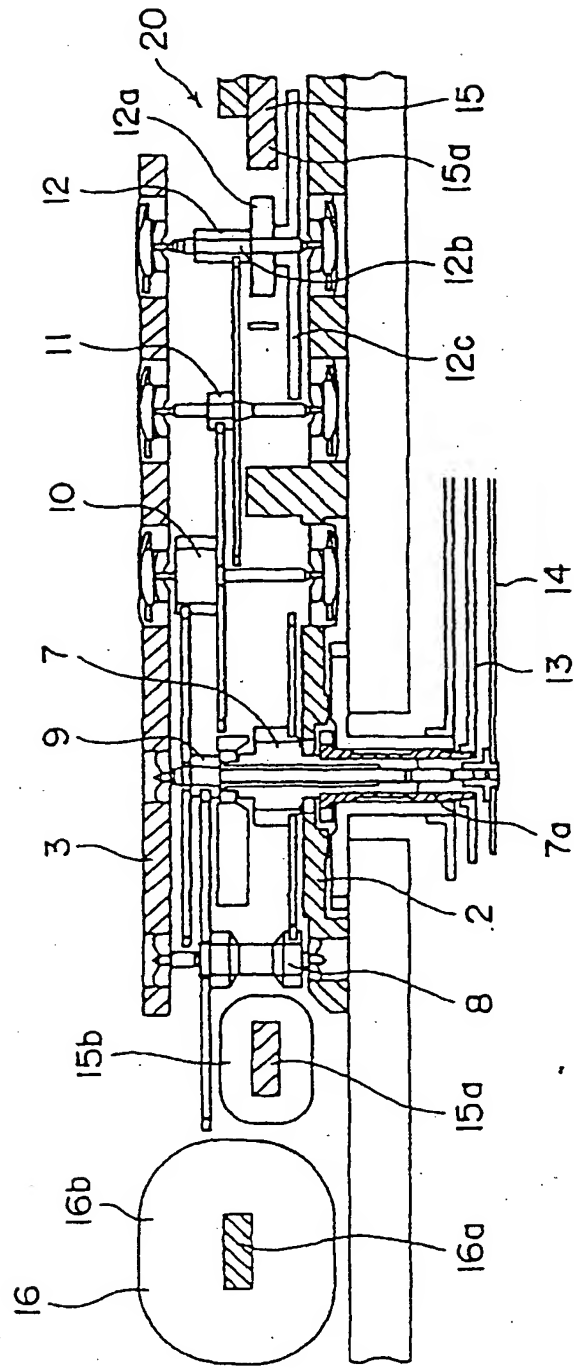




FIG. 4

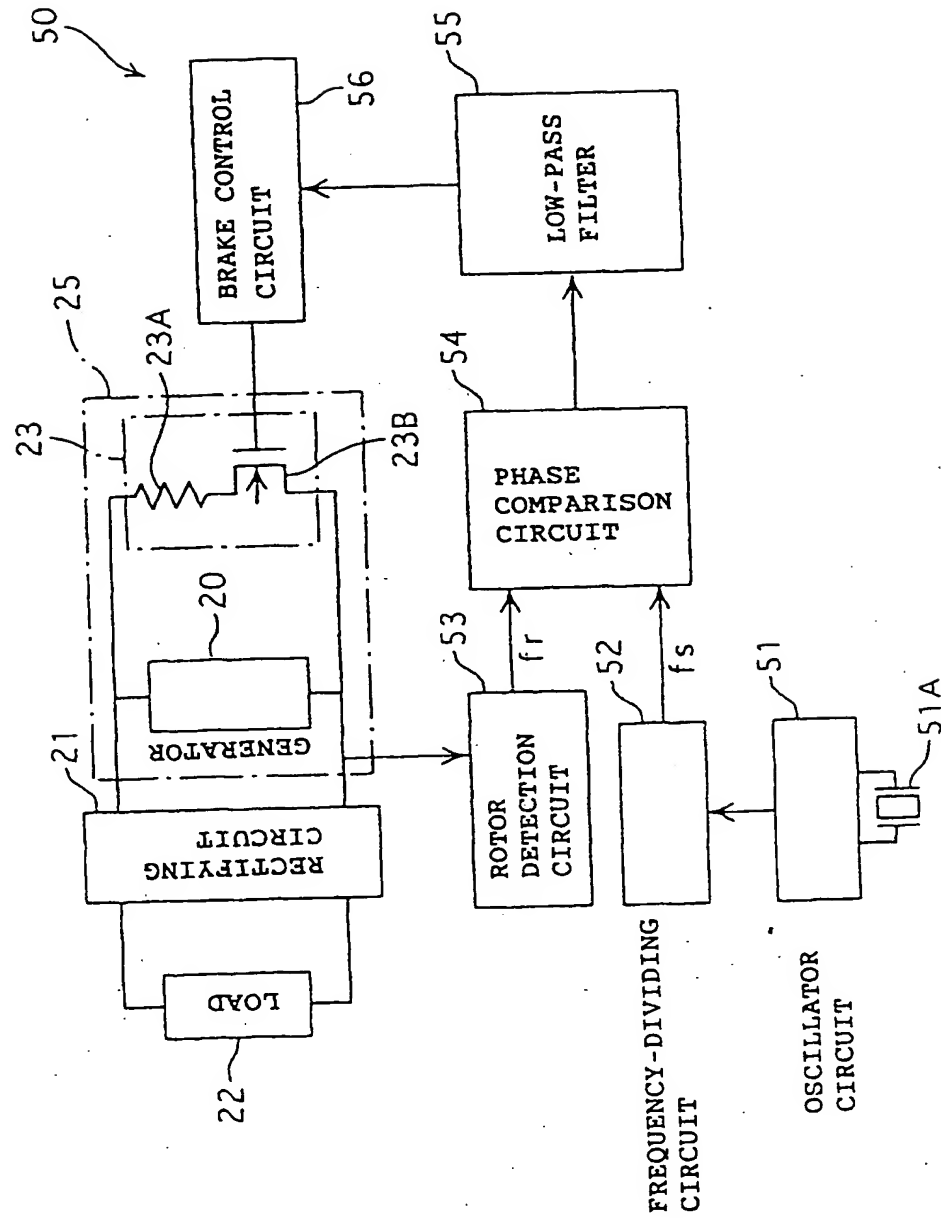
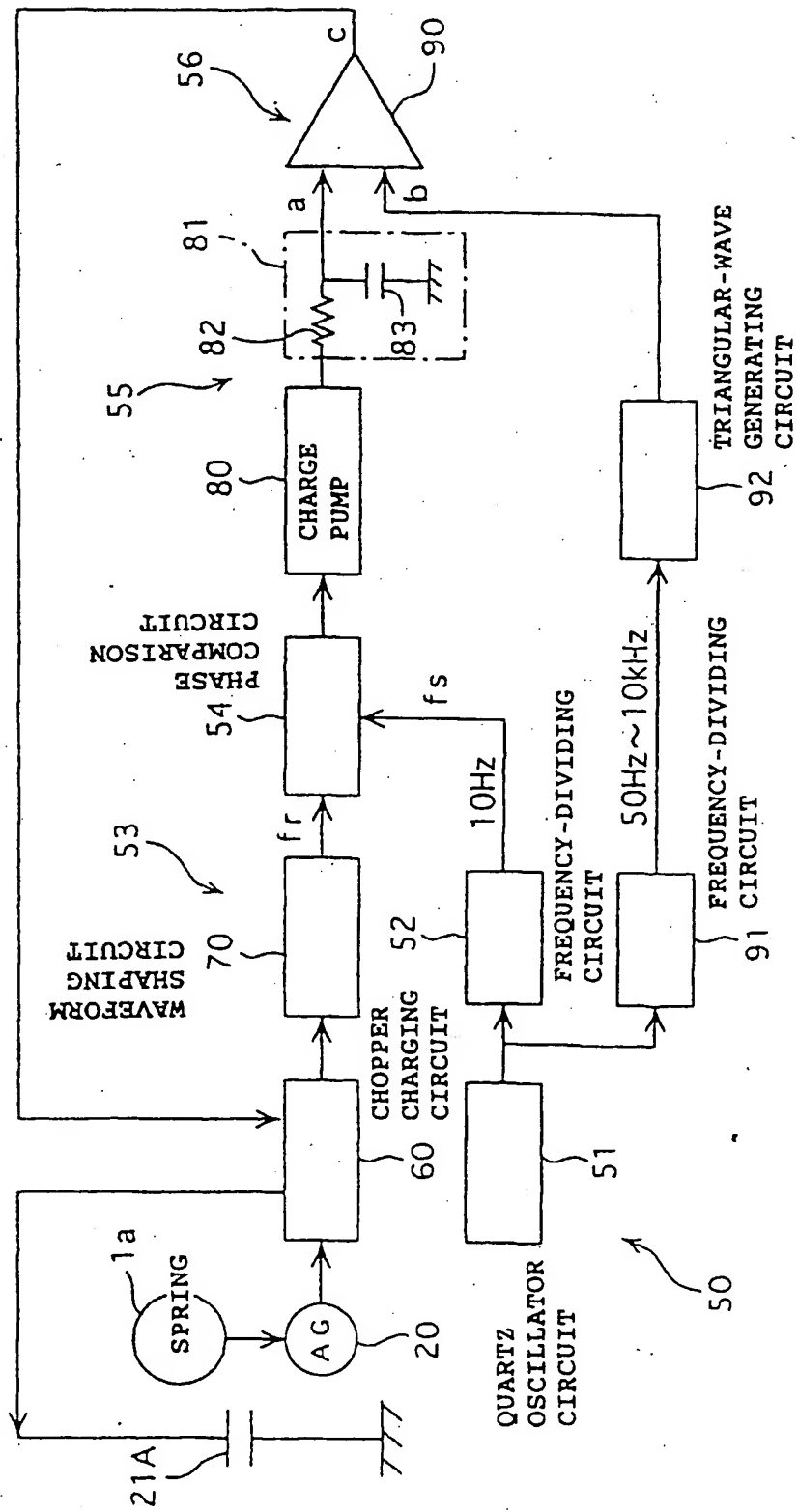


FIG. 5



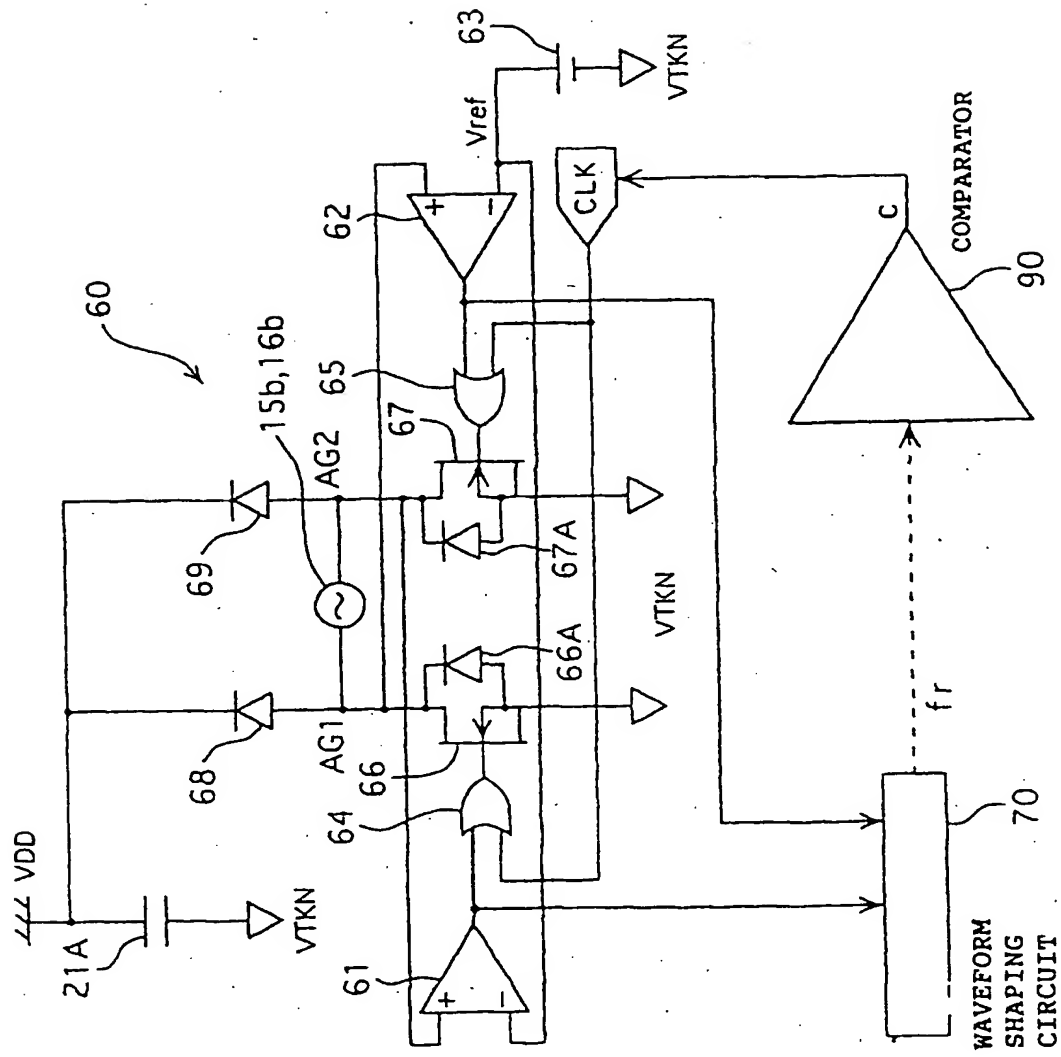


FIG. 6

FIG. 7

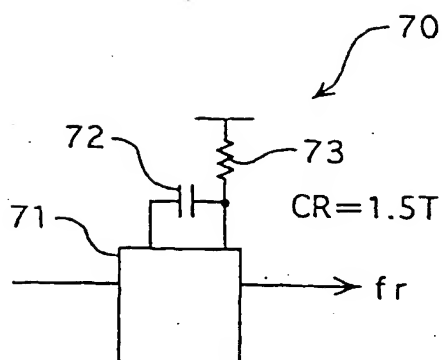
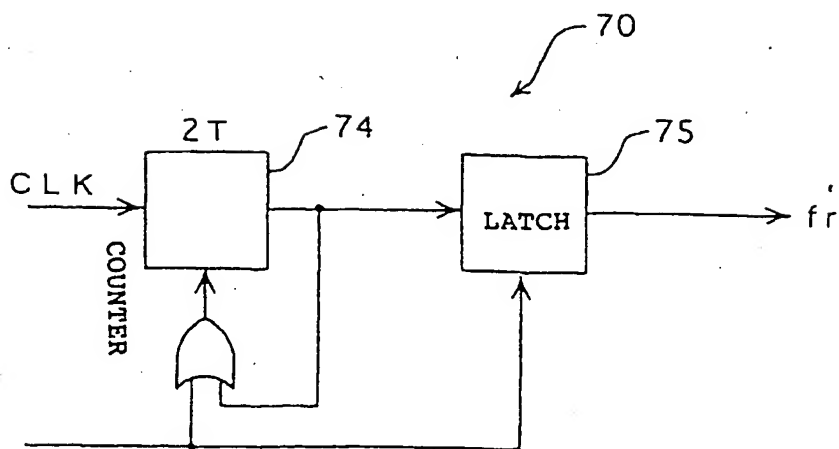


FIG. 8



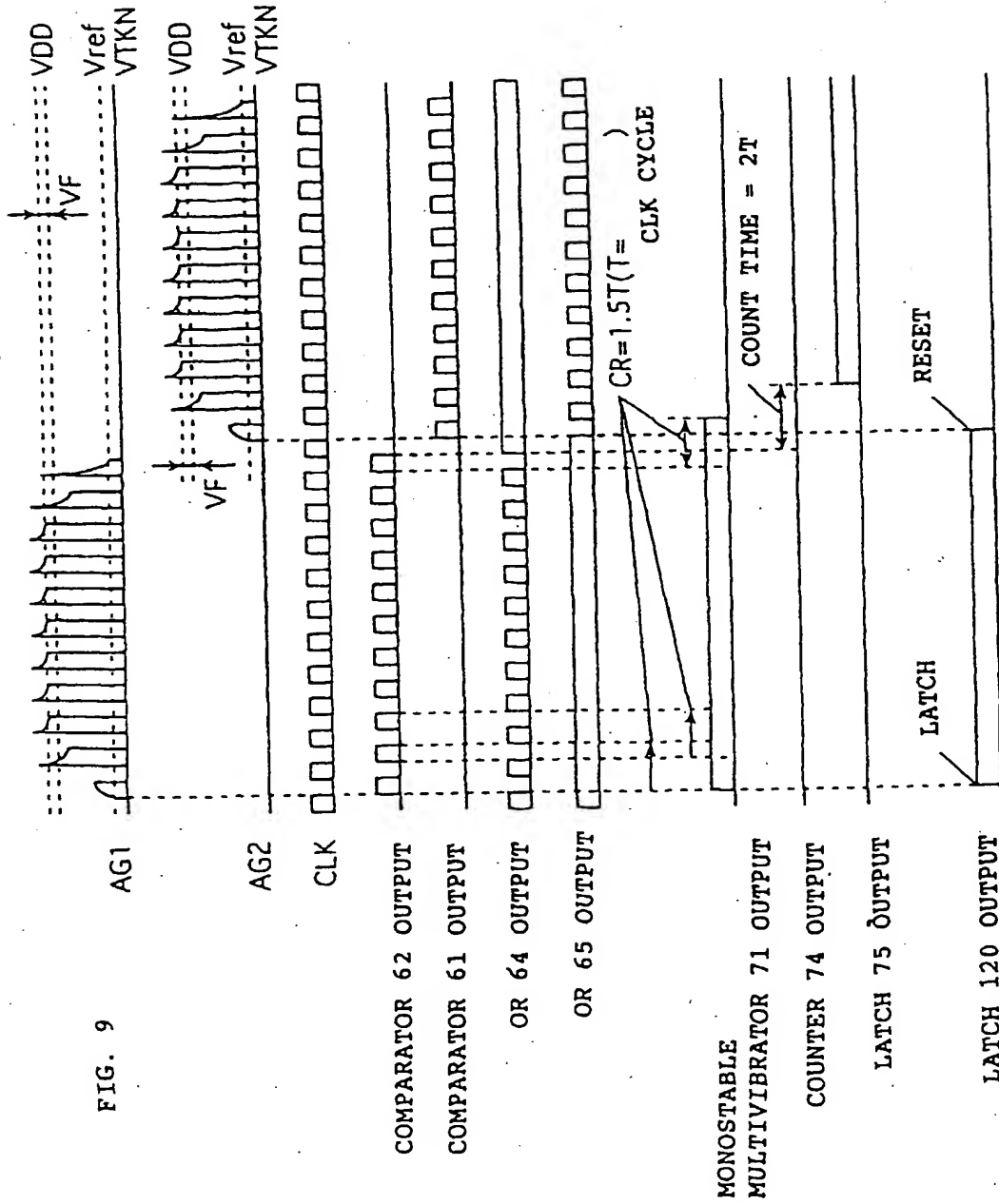


FIG. 10

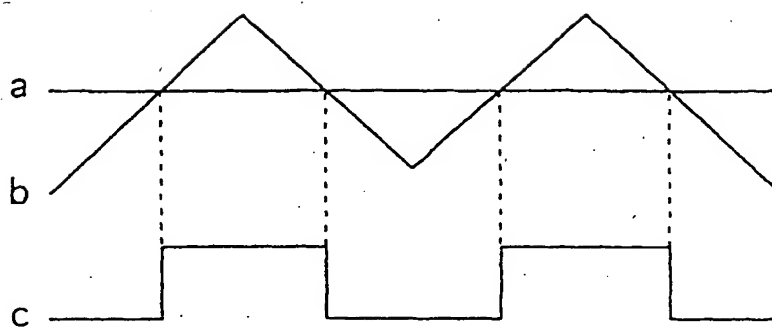




FIG. 11

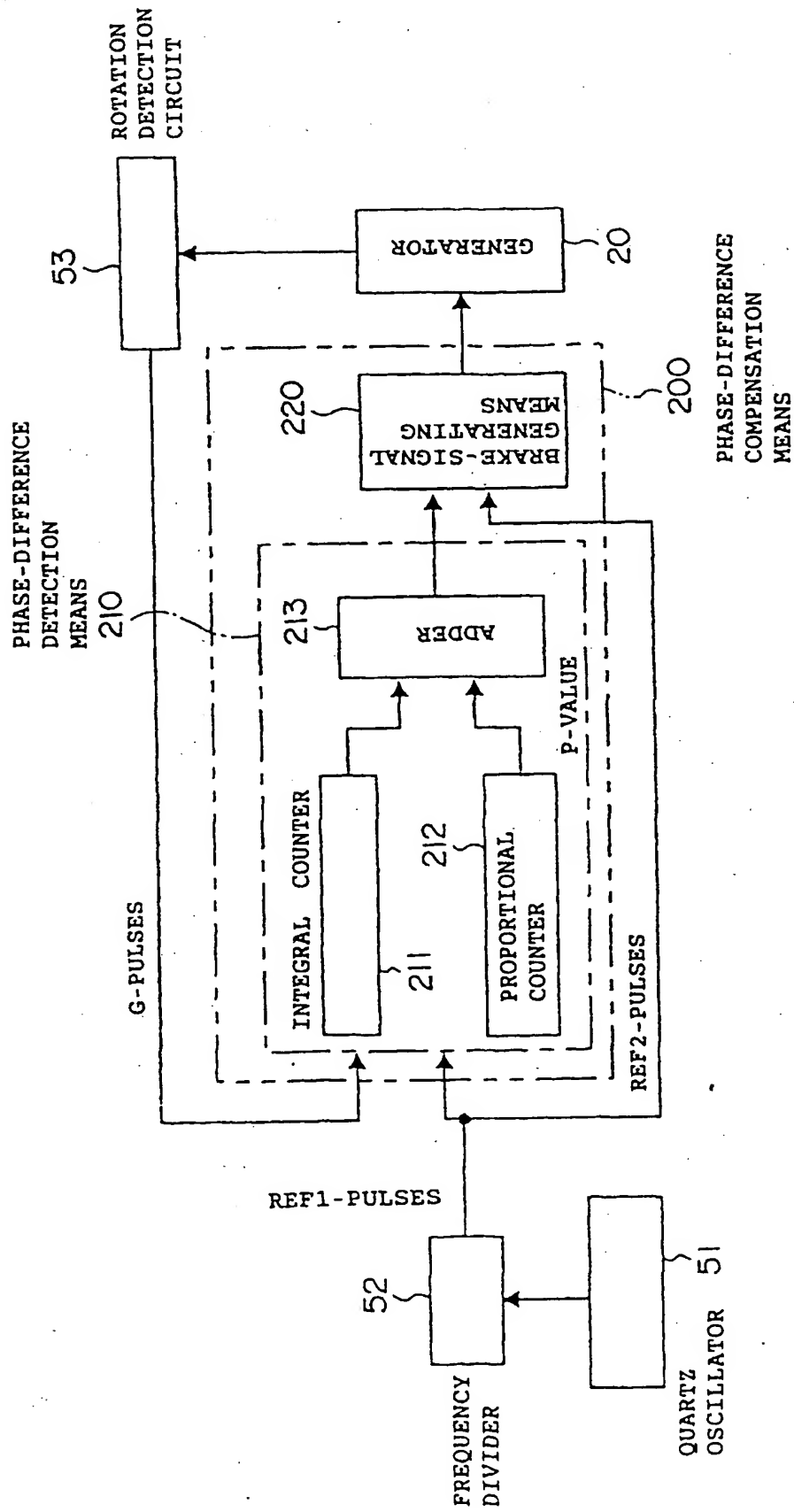


FIG. 12

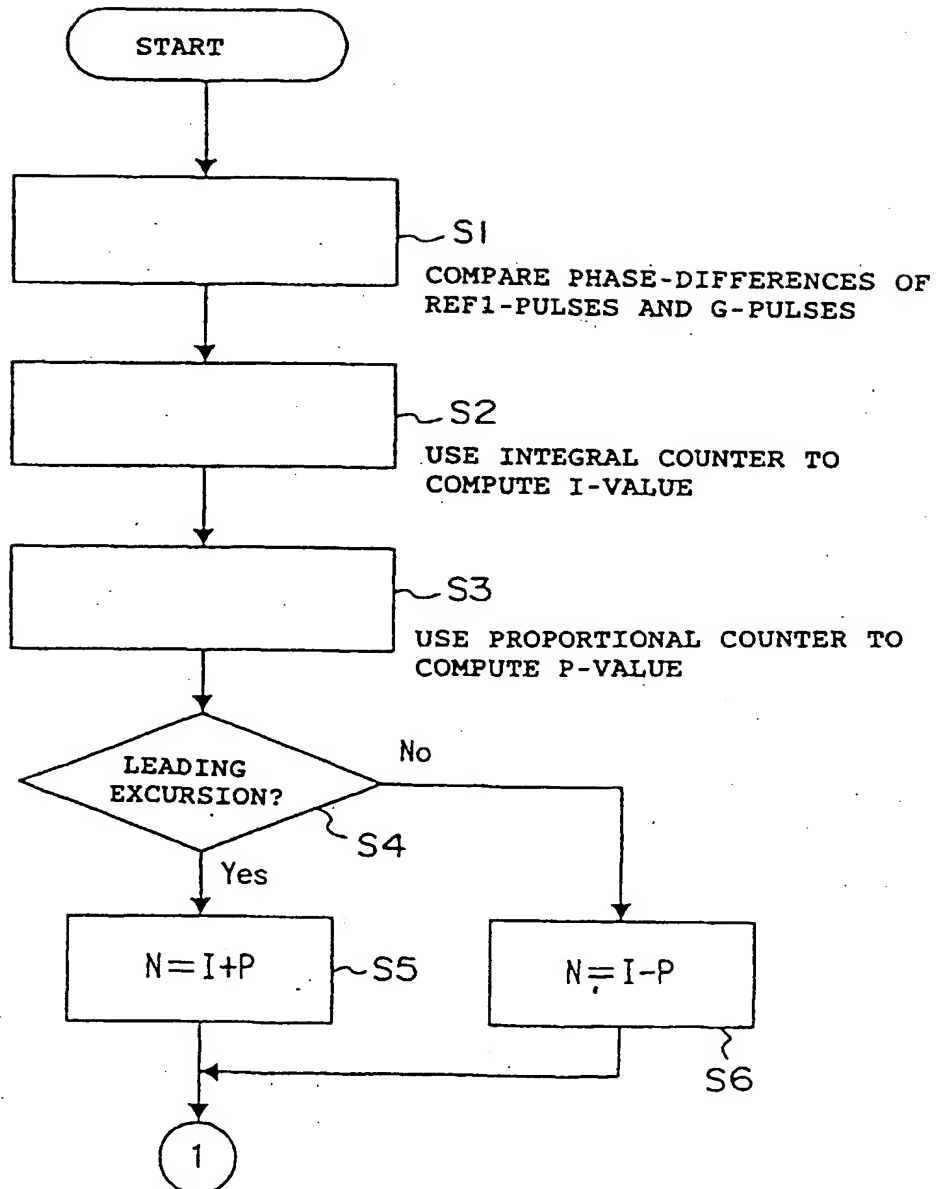


FIG. 13

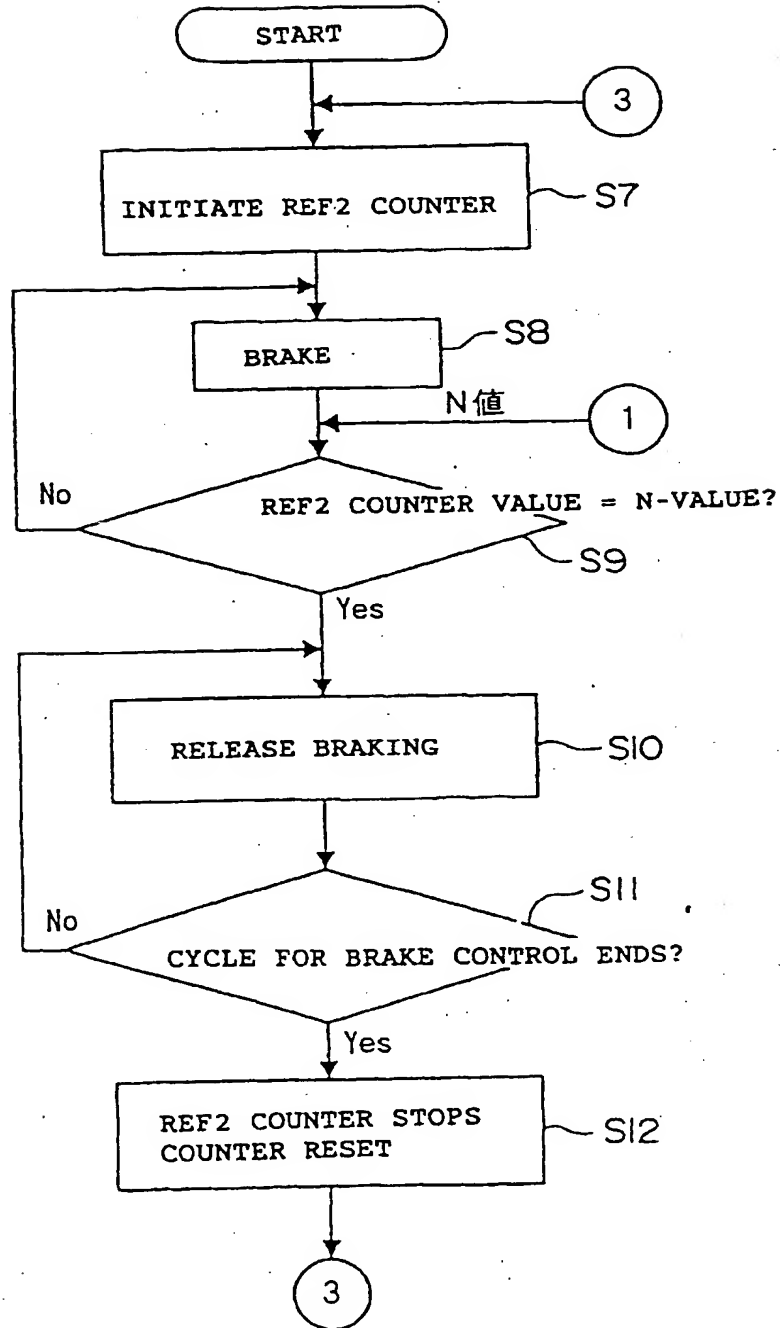
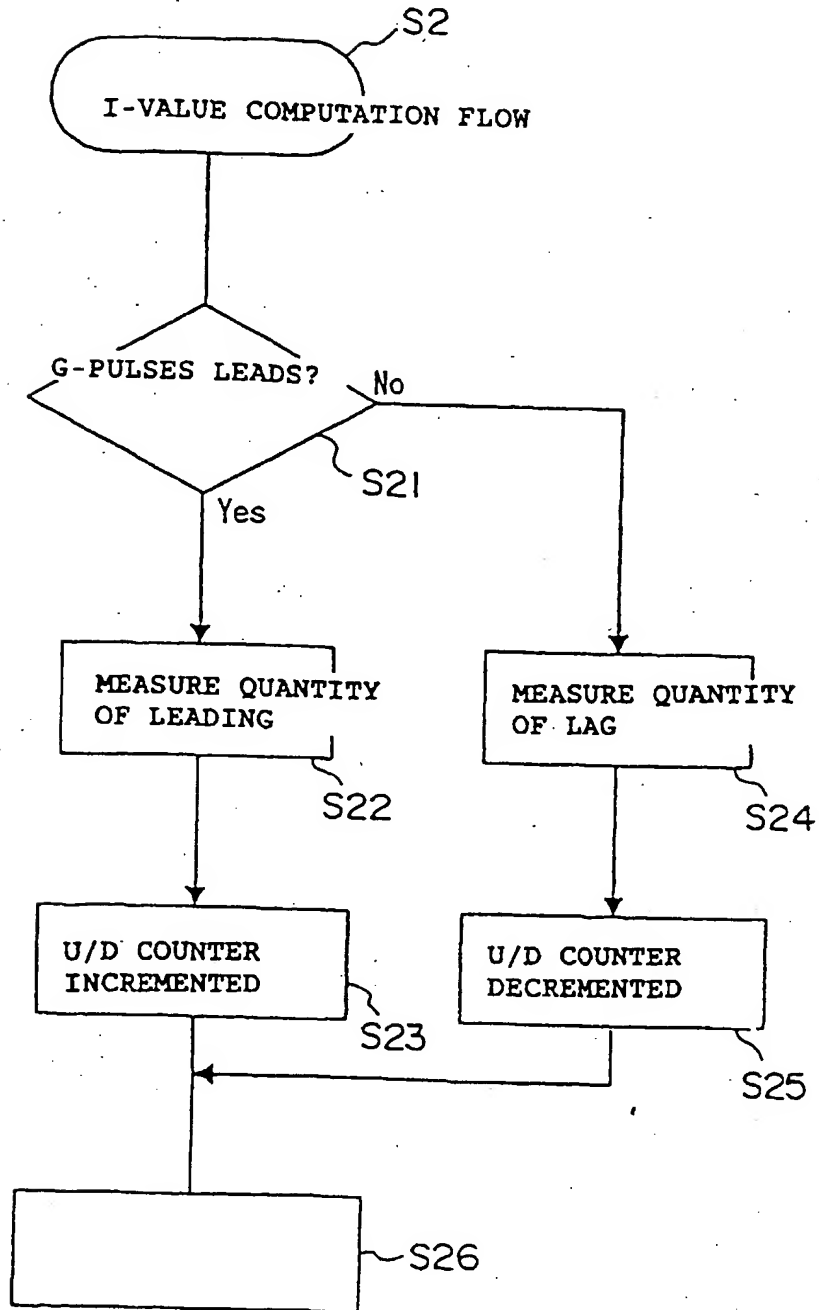


FIG. 14



I-VALUE = INTEGRAL COUNTER VALUE

FIG. 15

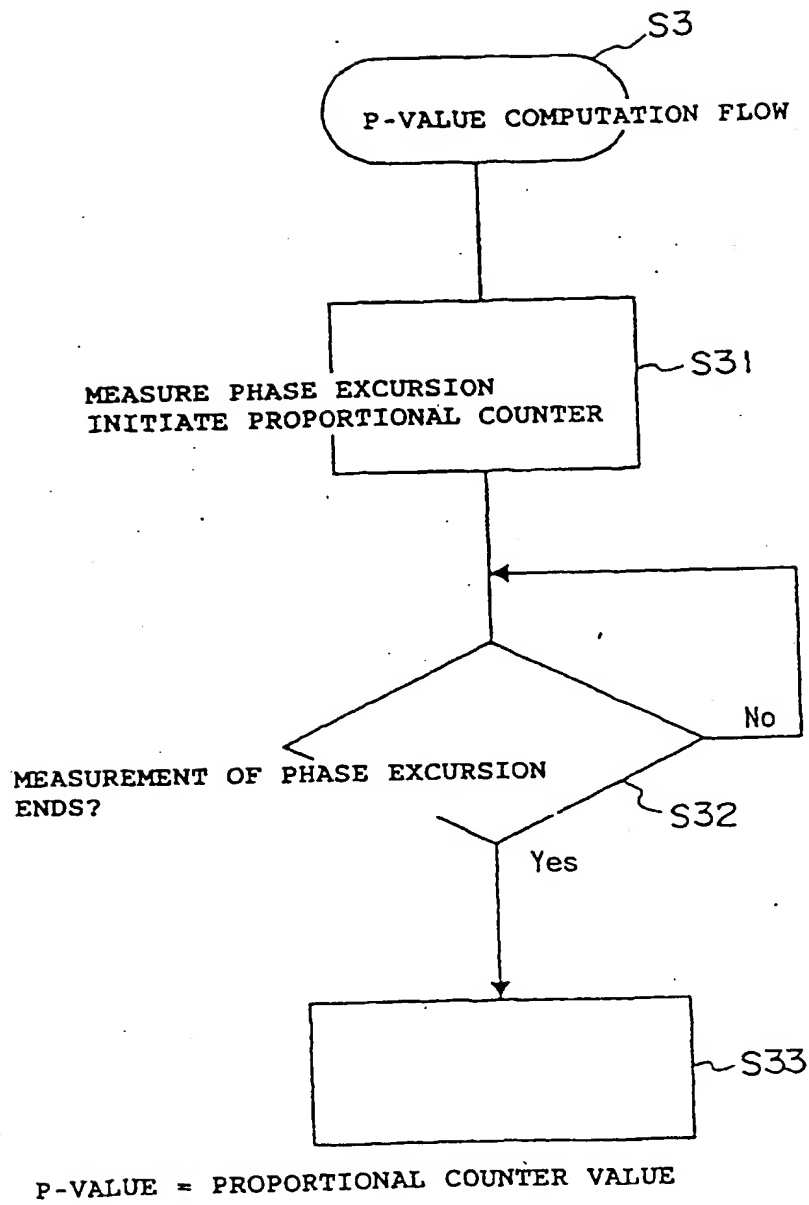


FIG. 16

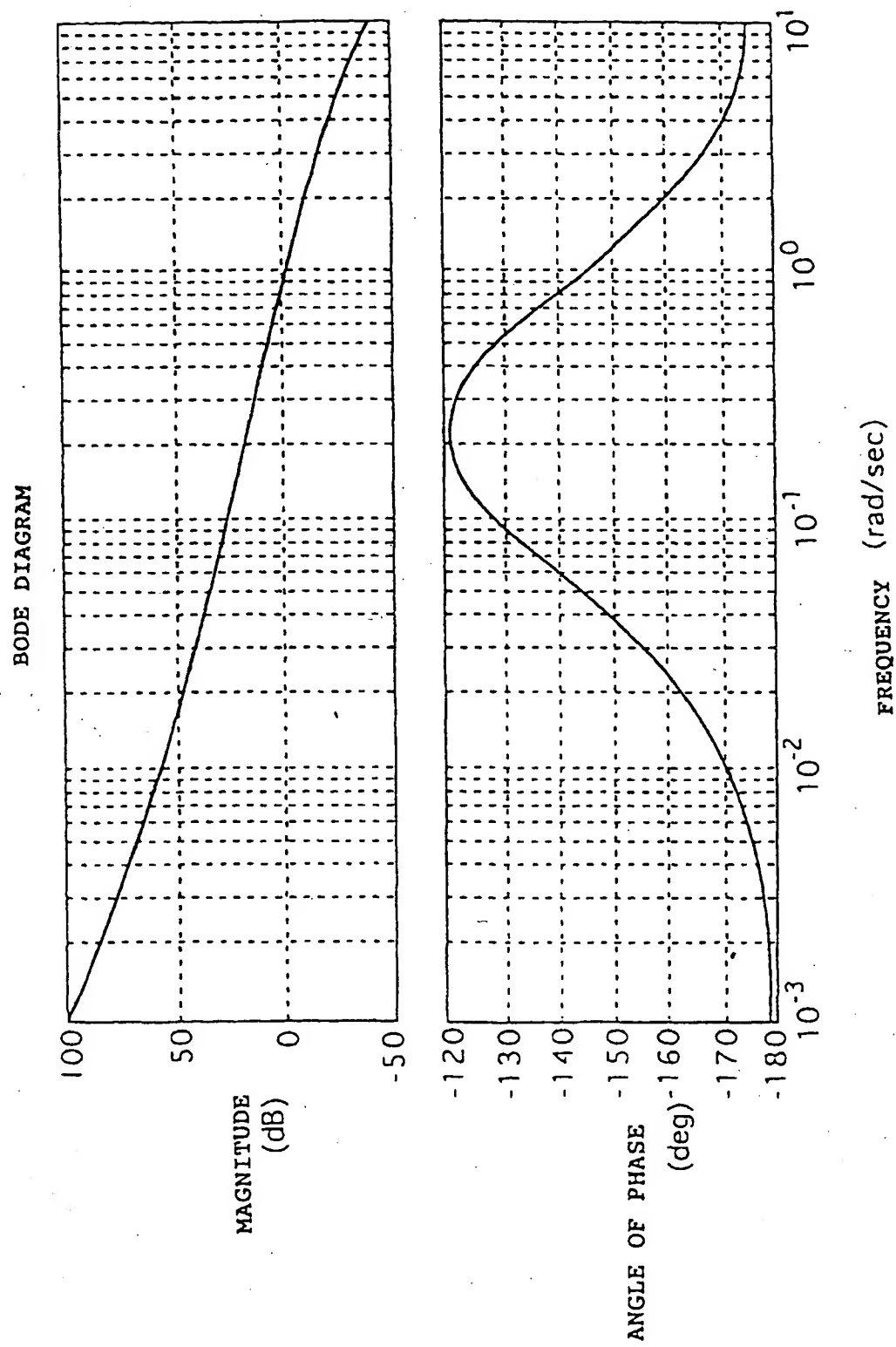




FIG. 17

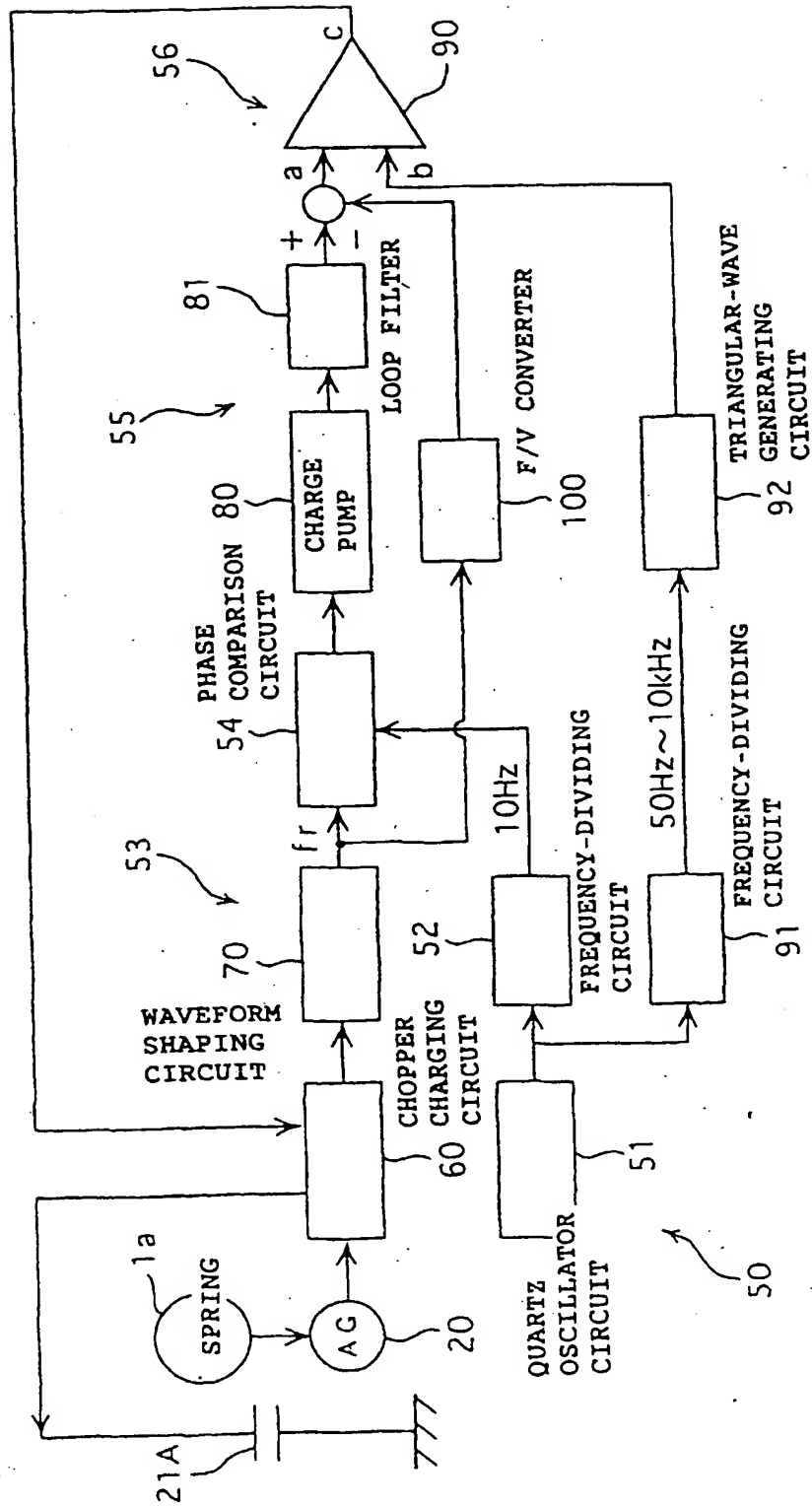


FIG. 18

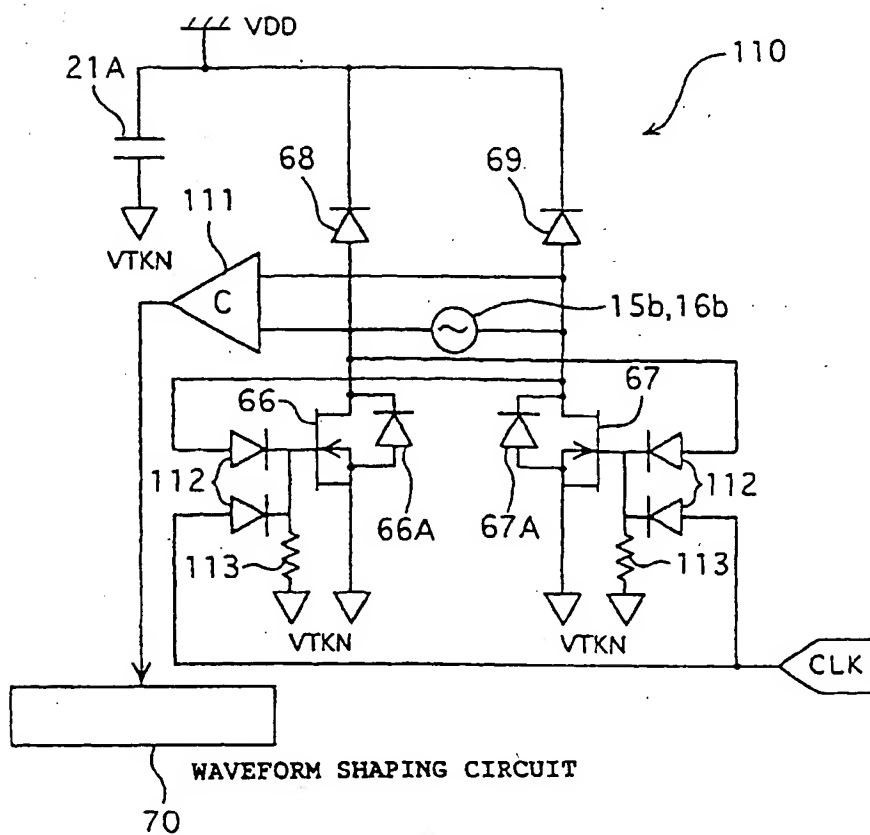


FIG. 19

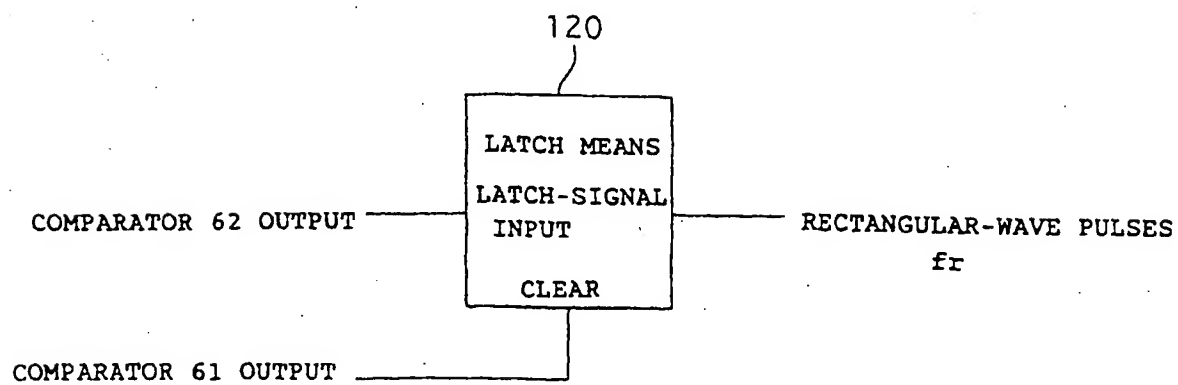


FIG. 20

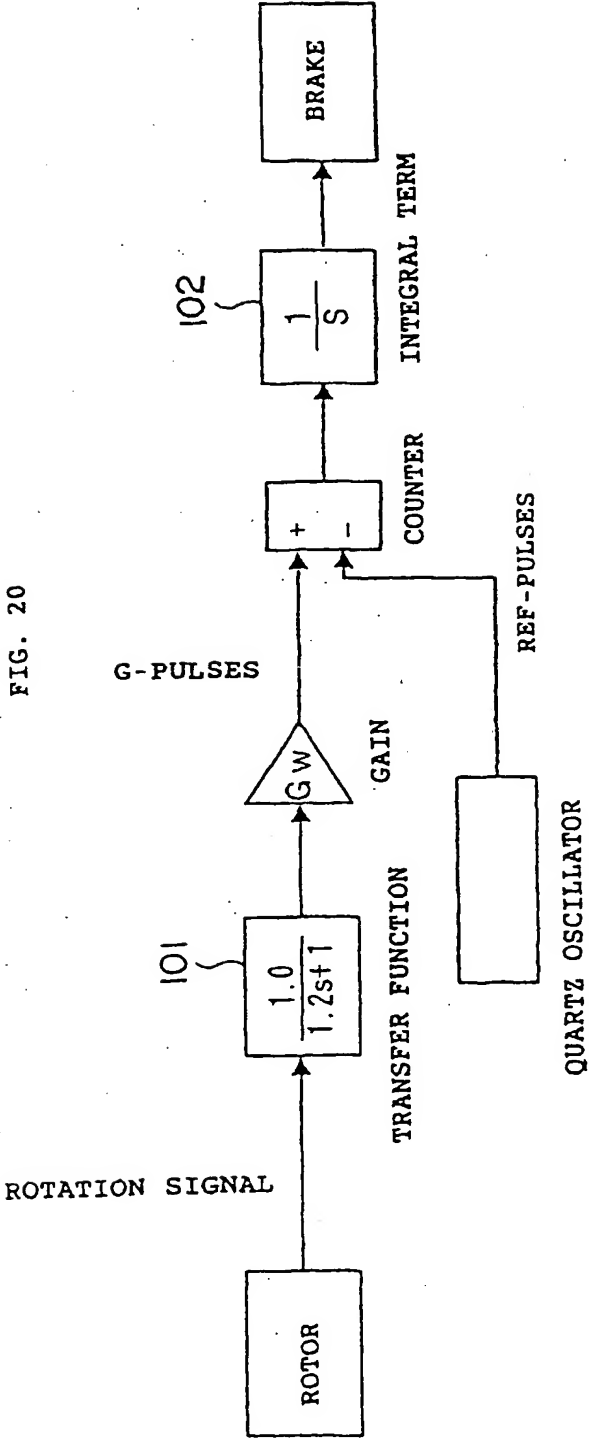


FIG. 21

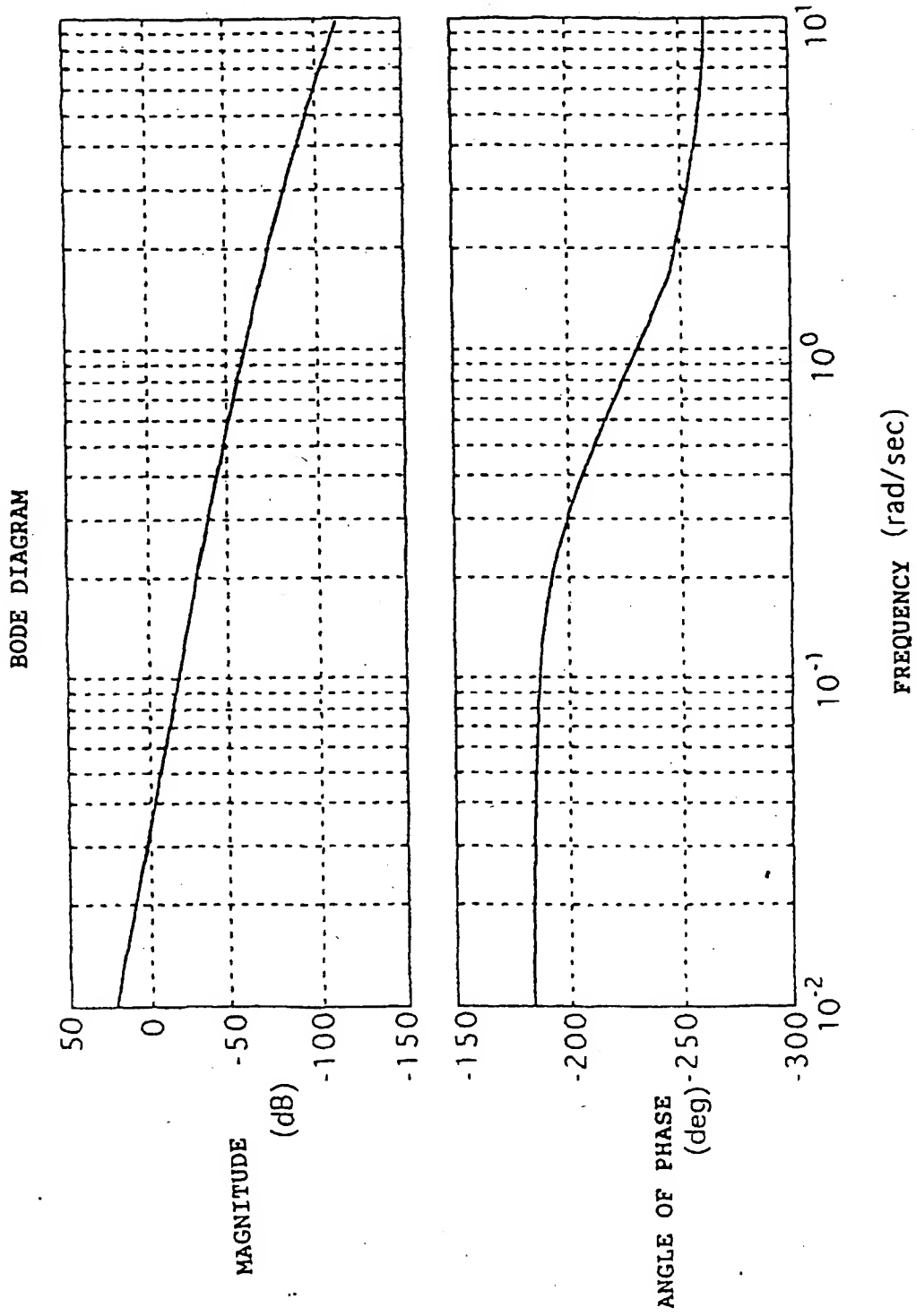
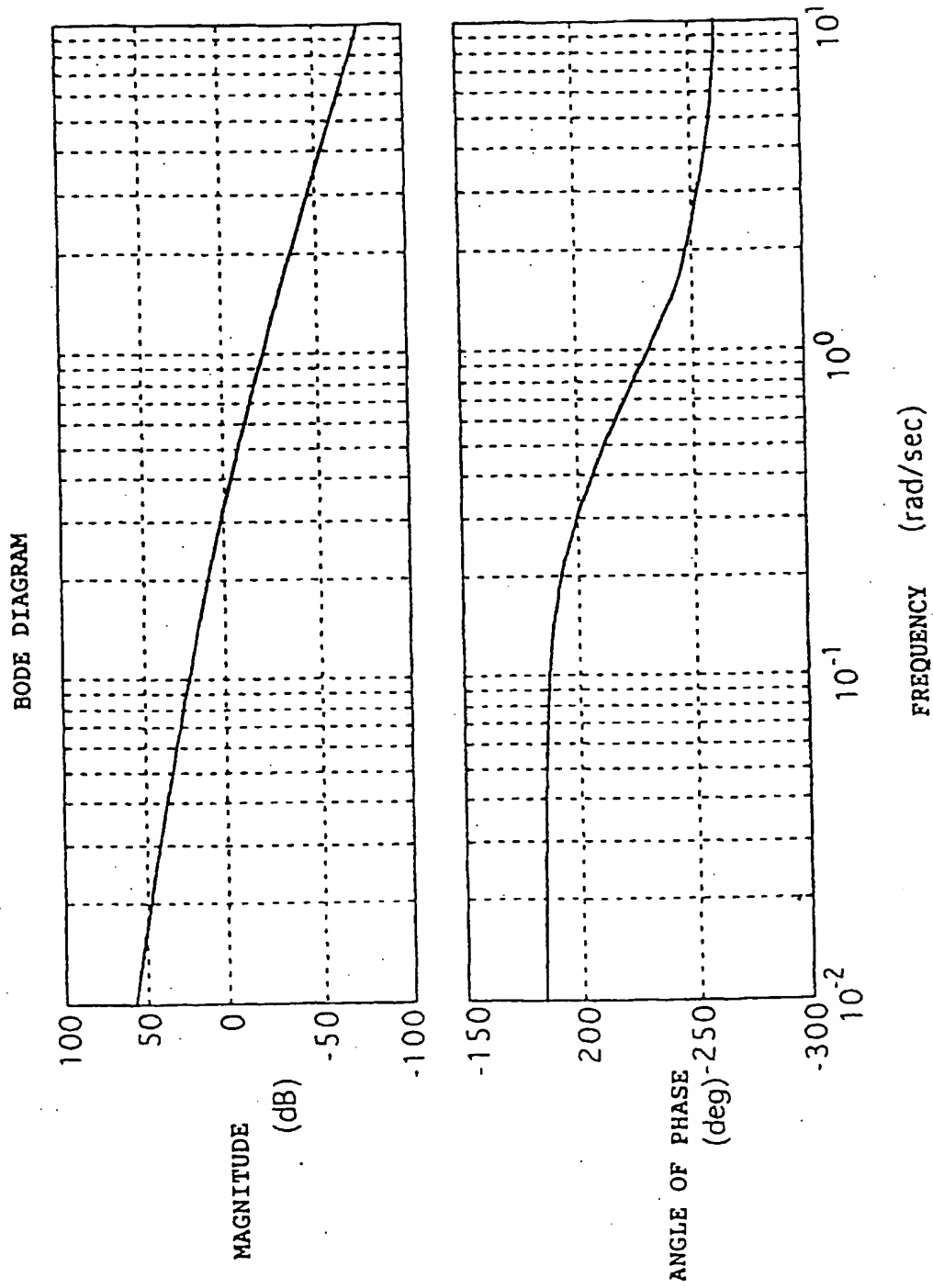


FIG. 22



## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP98/04395

A. CLASSIFICATION OF SUBJECT MATTER Int.Cl <sup>6</sup> G04C10/00, G04B17/00, G04B1/10, H02J7/00, H02K7/18, H02P9/00		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols) Int.Cl <sup>6</sup> G04C10/00, G04B17/00, G04B1/10, H02J7/00, H02K7/18, H02P9/00		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926-1996 Toroku Jitsuyo Shinan Koho 1994-1998 Kokai Jitsuyo Shinan Koho 1971-1998 Jitsuyo Shinan Toroku Koho 1996-1998		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP, 9-182305, A (Seiko Instruments Inc.), 11 July, 1997 (11. 07. 97), Full text ; all drawings & EP, 695978, A1 & US, 5615178, A	1-8
A	JP, 8-101284, A (Seiko Instruments Inc., Seiko Epson Corp.), 16 April, 1996 (16. 04. 96), Full text ; all drawings (Family: none)	1-8
A	JP, 8-36072, A (Seiko Instruments Inc., Seiko Epson Corp.), 6 February, 1996 (06. 02. 96), Full text ; all drawings (Family: none)	1-8
A	JP, 7-119812, B2 (Asulab S.A.), 20 December, 1995 (20. 12. 95), Full text ; all drawings & EP, 239820, A1 & JP, 62-255889, A & EP, 239820, B2 & CH, 665082, A3	1-8
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
<p>* Special categories of cited documents:</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</p> <p>"&amp;" document member of the same patent family</p>		
Date of the actual completion of the international search 26 October, 1998 (26. 10. 98)		Date of mailing of the international search report 10 November, 1998 (10. 11. 98)
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer
Facsimile No.		Telephone No.



## INTERNATIONAL SEARCH REPORT

International application No.  
PCT/JP98/04395

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP, 49-84680, A (Daini Seikosha K.K.), 14 August, 1974 (14. 08. 74), Full text ; all drawings (Family: none)	1-8
A	Microfilm of the specification and drawings annexed to the request of Japanese Utility Model Application No. 65760/1982 (Laid-open No. 168398/1983) (Takara Co., Ltd.); 10 November, 1983 (10. 11. 83), Full text ; all drawings (Family: none)	1-8

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